

Measurement and Analysis of GPU-accelerated Applications with HPCToolkit

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Consortium for the Advancement of Scientific Software
Birds of a Feather Days
February 11, 2026



U.S. DEPARTMENT OF
ENERGY

Office of
Science



DOE's GPU-Accelerated Exascale Platforms



- Frontier compute nodes (OLCF)
 - 1 AMD EPYC “Trento” CPU
 - 4 MI250X AMD Radeon Instinct GPUs
 - 4 Slingshot 11 endpoints
 - Unified memory architecture



- Aurora compute nodes (ALCF)
 - 2 Intel Xeon “Sapphire Rapids” processors
 - 6 Intel Data Center GPU Max 1500
 - 8 Slingshot 11 endpoints
 - Unified memory architecture



- El Capitan compute nodes (LLNL)
 - 4 AMD MI300A APU
 - 4 Slingshot 11 endpoints
 - Unified memory architecture

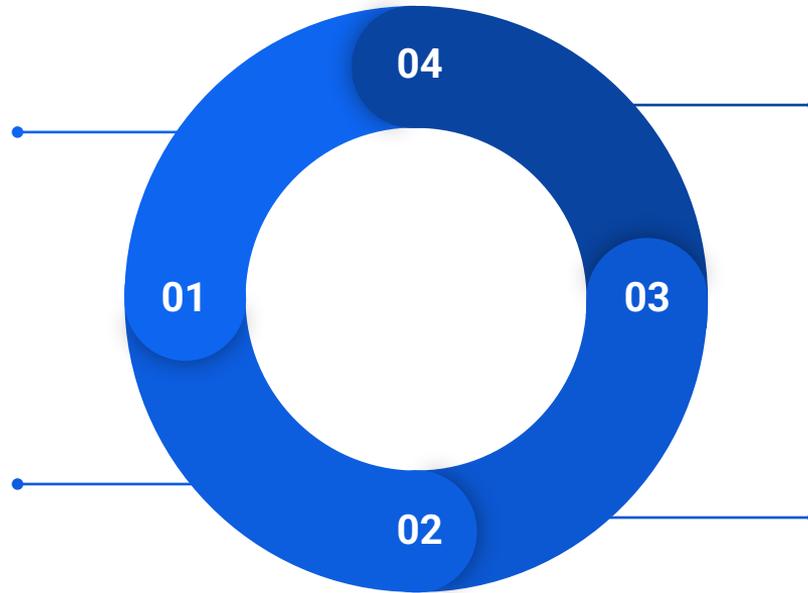
Tuning HPC Applications

Profile the Application

Collect detailed performance data on an application execution

Analyze Performance Data

Aggregate performance data from all application threads and generate a database of "analysis results"



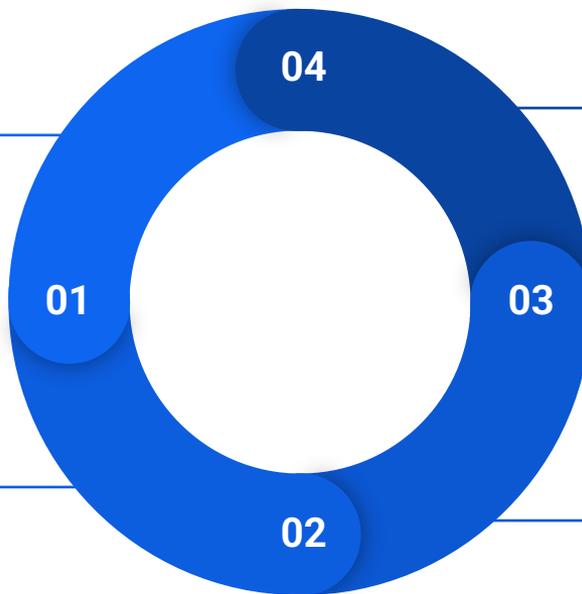
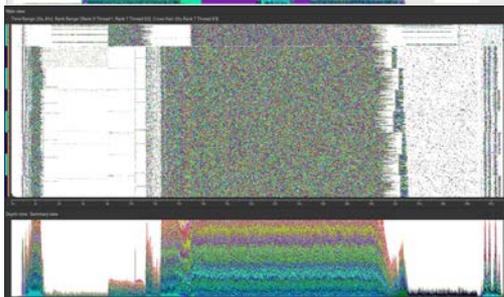
Optimize and Improve

Remove performance bottlenecks and further improve the application

Inspect and Identify

Inspect locations with poor performance and identify root causes

Tuning HPC Applications



```
Pathname: /data/per-kernel/Algorithm_REDC1_SIM/and/hpc/rosl1-roja-perf-see-database
Measurements: /data/per-kernel/Algorithm_REDC1_SIM/and/hpc/rosl1-roja-perf-see-measurements

PROGRAM TOTALS
-----
Total Execution Time: 0.0037s
Total Stall Cycles: 1,479,480,312
Total GPU Cycles: 1,946,157,056
Kernel Stall Ratio: 26.2%
Kernels Analyzed: 1
Kernels Skipped: 0

TOP 1 KERNELS BY STALL CYCLES
-----
# Kernel          Time (s)    Stall Cycles  Stall %    Occupancy  Sample Rate
1: test          0.0037      1479480312    97.0%      30% (vgpr)  181.36% / 20%

KERNEL #1: test
GPU Binary: C5F8354ad847c9d66271279a311.gdubin
Occupancy: 30% (31/98 waves/CG), limited by vgr! [CGPR=48, 50PR=58, 101=448]

Low GPU Performance Analysis
-----
WARNING: Very low profile coverage: 66/1339 (4.9%)
Kernel: test
Total Stall Cycles: 423,624,784
Architecture: AMD GCN90

STALL ANALYSIS (PC Sampling - Back-aliasing - Root Cause)
-----
Stall Location      Stall Opcode      Root Cause Location      Root Opcode      Cycles % Total Speedup
REDC1_SIM/roja-cpp/184-15  k_wdload_opens100  ==> REDC1_SIM/roja-cpp/184-15  @global_load_derived  277,852,660  65.0%  1.71x
amd_device_functions.h:778:9  k_barrier          ==> amd_device_functions.h:778:9  k_barrier            44,948,152  10.4%  1.11x
kernel.c:109:192             @block_load_func_app_258  @global_load_derived2  51,077,238  12.0%  1.04x
block_load_func_app_258     @global_load_app_258    ==> block_load_func_app_258     v_mmc_to_u32_s32      18,465,675  4.3%  1.04x
loop_3x27_0:110 (stream)
```



Why HPCToolkit?

- **Widely applicable:** many parallel programming models within & across nodes
- **Easy:** profiles unmodified application binaries
- **Fast:** low-overhead measurement; parallel analysis of large performance data
- **Scalable:** measure and analyze GPU-accelerated executions at large scale
- **Informative:** learn where an application spends its time and why
 - call path profiles associate metrics with application source code contexts
 - hierarchical traces reveal execution dynamics
 - LEO identifies and quantifies root causes of GPU performance losses
- **Broad audience:** developers of applications, frameworks, runtimes & tools
- **Multiplatform:** unlike vendor tools, works with a wide range of CPUs and GPUs

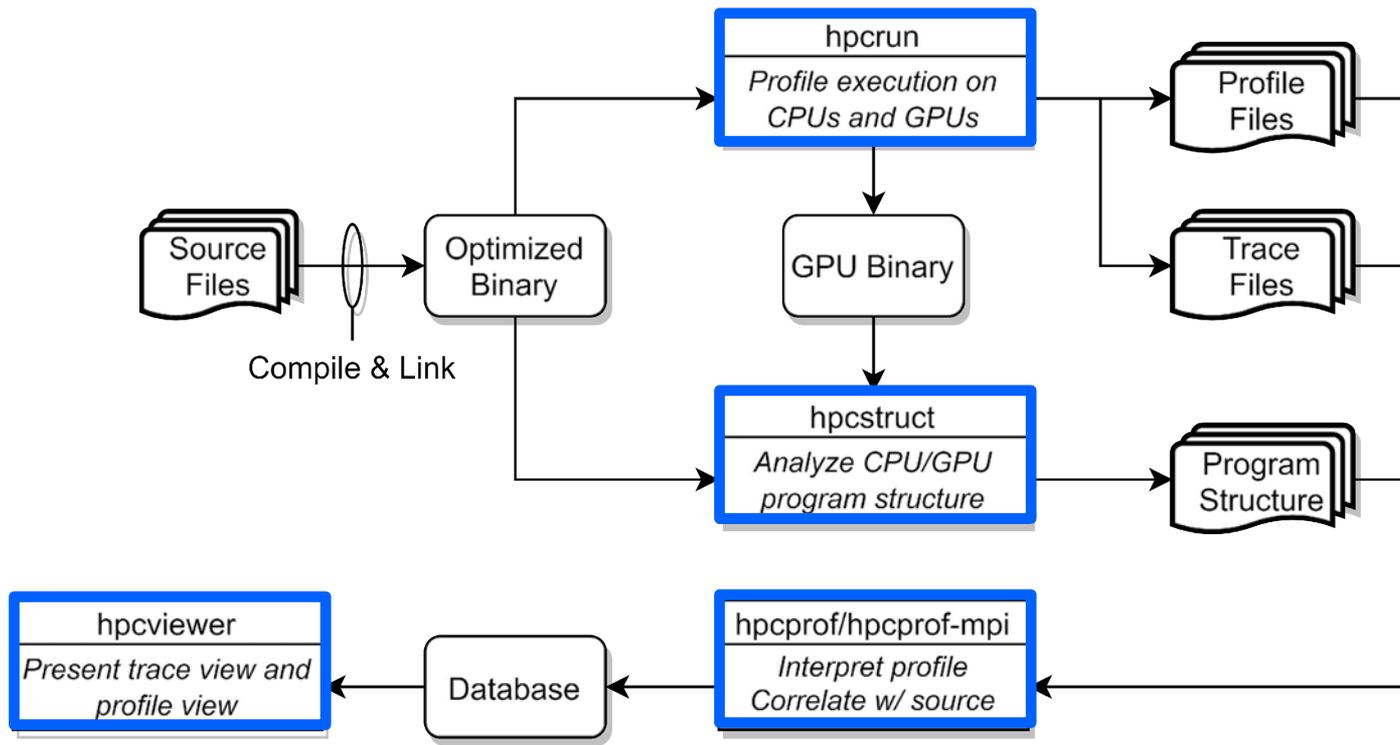
How is HPCToolkit Different from Vendor Tools?

- More scalable tracing than vendor tools
 - measure exascale executions across many nodes and GPUs
 - GUI supports interactive exploration of TB of performance data
- Scalable, parallel post-mortem analysis vs. non-scalable in-GUI analysis
- Detailed reconstruction of calling context profiles within GPU kernels
- Identifies and quantifies root causes of GPU performance losses

Today's Agenda

- Introduce HPCToolkit tools and workflow
- Illustrate HPCToolkit's use with some case studies
- Live demos
 - profiling and tracing
 - instruction-based performance metrics
- Instruction-level performance monitoring
 - explaining instruction-level performance
 - improving analysis and attribution of PC samples
 - automating analysis of GPU bottlenecks
- Discussion about needs, problems, and suggestions

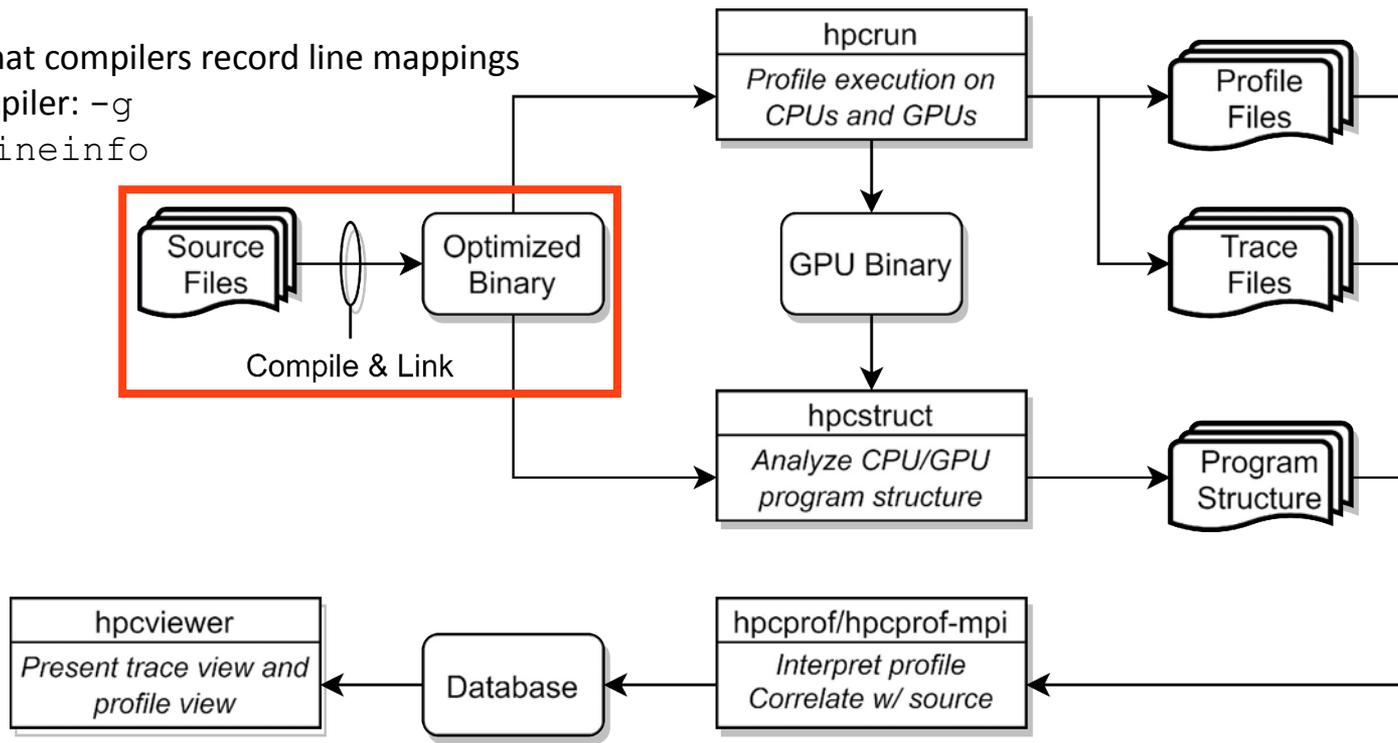
HPCToolkit's Workflow for GPU-accelerated Applications



HPCToolkit's Workflow for GPU-accelerated Applications

Step 1:

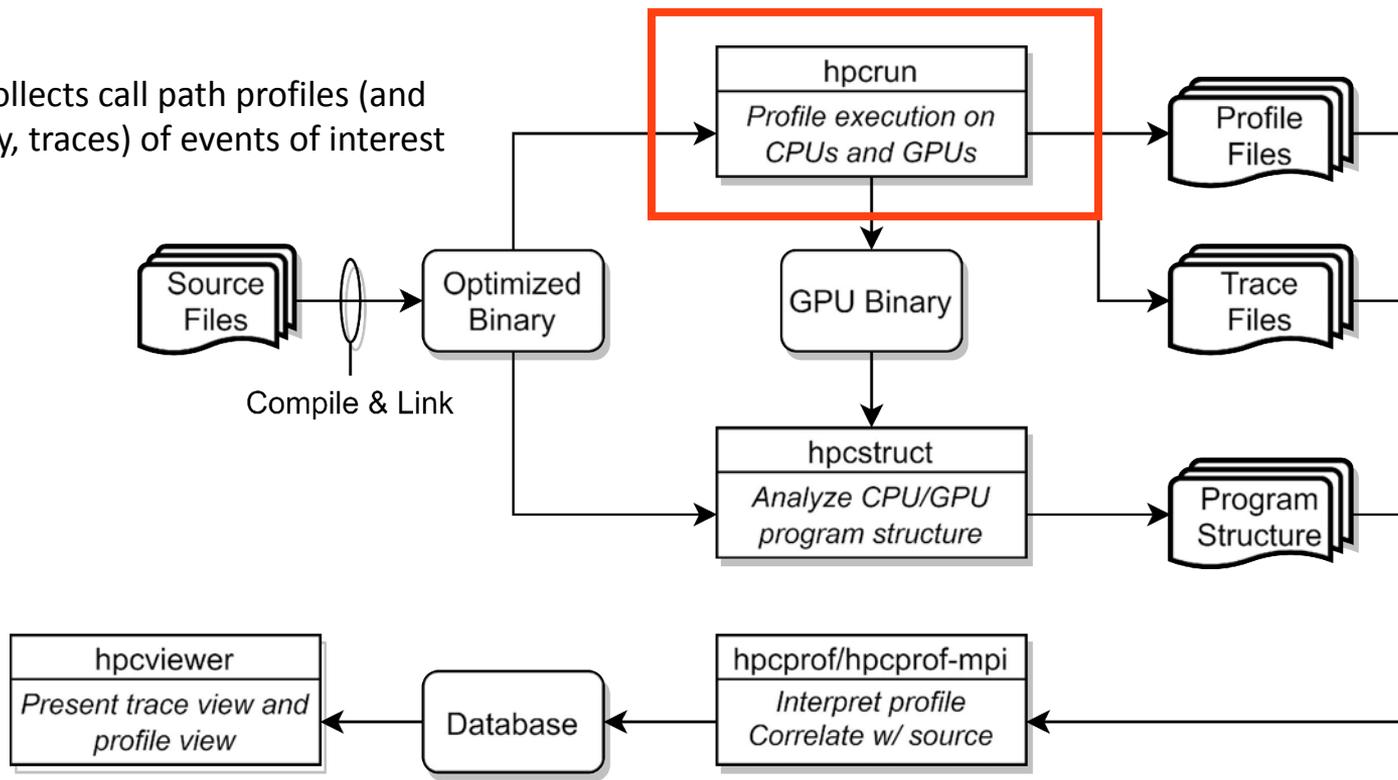
- Ensure that compilers record line mappings
- host compiler: `-g`
- `nvcc: -lineinfo`



HPCToolkit's Workflow for GPU-accelerated Applications

Step 2:

- *hpcrun* collects call path profiles (and optionally, traces) of events of interest



Measurement of CPU and GPU-accelerated Applications

- Sampling using Linux timers and hardware counters on the CPU
- Callbacks when GPU operations are launched
- Event stream or callbacks for GPU operation completion
- PC Samples: AMD, NVIDIA, Intel
- Binary instrumentation of GPU kernels: Intel

hpcrun: Measure CPU and/or GPU activity

- CPU and GPU profiling

— hpcrun -e CPUTIME -e gpu=**xxx** <app> ...

- CPU and GPU profiling and tracing

— hpcrun -e CPUTIME -e gpu=**xxx** **-tt** <app>

- GPU PC sampling

— hpcrun -e gpu=**yyy**,pc <app>

- Measuring MPI programs

— srun -n <ranks> ... hpcrun -e CPUTIME -e gpu=**xxx** <app>

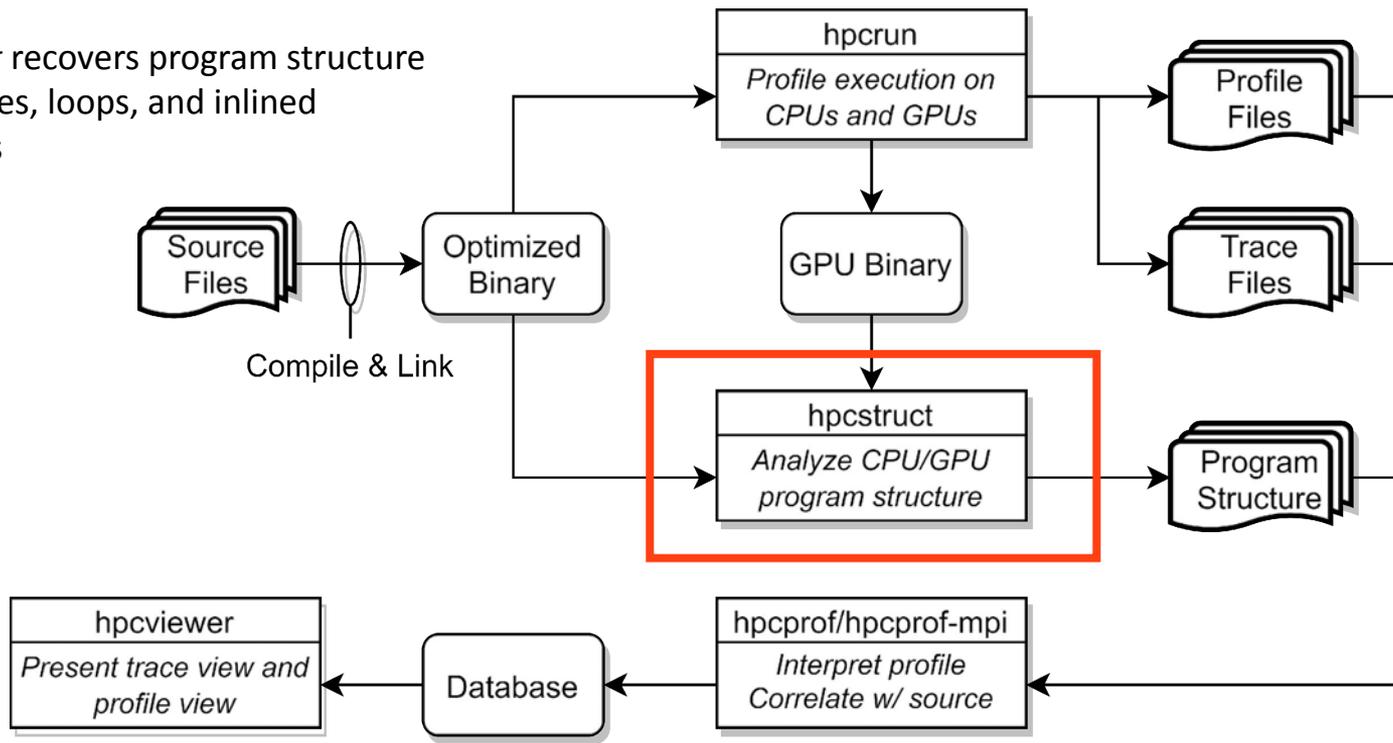
xxx ∈ {*cuda, rocm, opencl, level0*}

yyy ∈ {*cuda, rocm, level0*}

HPCToolkit's Workflow for GPU-accelerated Applications

Step 3:

- *hpcstruct* recovers program structure about lines, loops, and inlined functions



hpcstruct: Analyze CPU and GPU Binaries Using Multiple Threads

- Usage

```
hpcstruct [--gpucfg yes] <measurement-directory>
```

- What it does

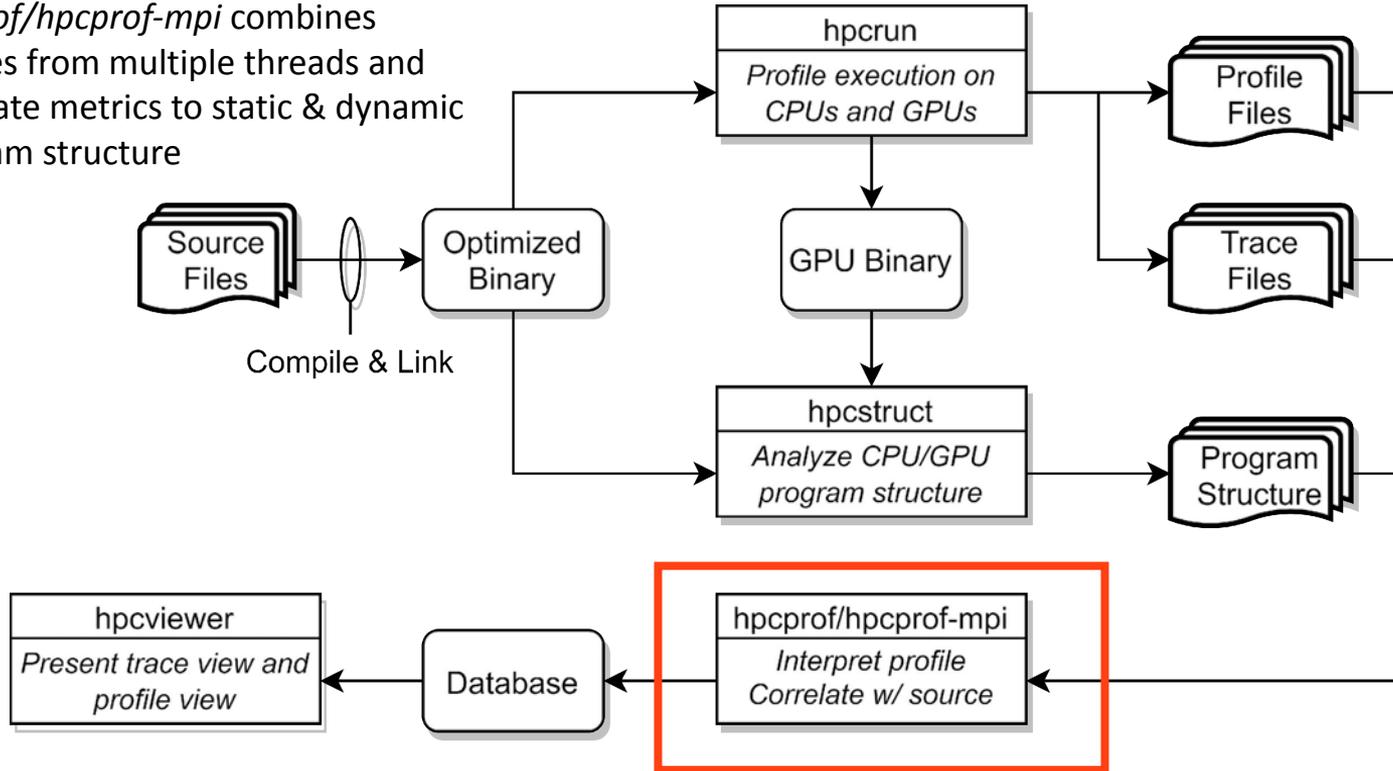
- Recover program structure information
 - Files, functions, inlined templates or functions, loops, source lines
- In parallel, analyze all CPU and GPU binaries that were measured by HPCToolkit
 - typically analyze large application binaries with 16 threads
 - typically analyze multiple small application binaries concurrently with 2 threads each
- Cache binary analysis results for reuse when analyzing other executions

NOTE: `--gpucfg yes` needed only for detailed analysis of PC samples

HPCToolkit's Workflow for GPU-accelerated Applications

Step 4:

- *hpcprof/hpcprof-mpi* combines profiles from multiple threads and correlate metrics to static & dynamic program structure



hpcprof/hpcprof-mpi: Associate Measurements with Program Structure

- Analyze data from modest-scale executions with multithreading

```
hpcprof <measurement-directory>
```

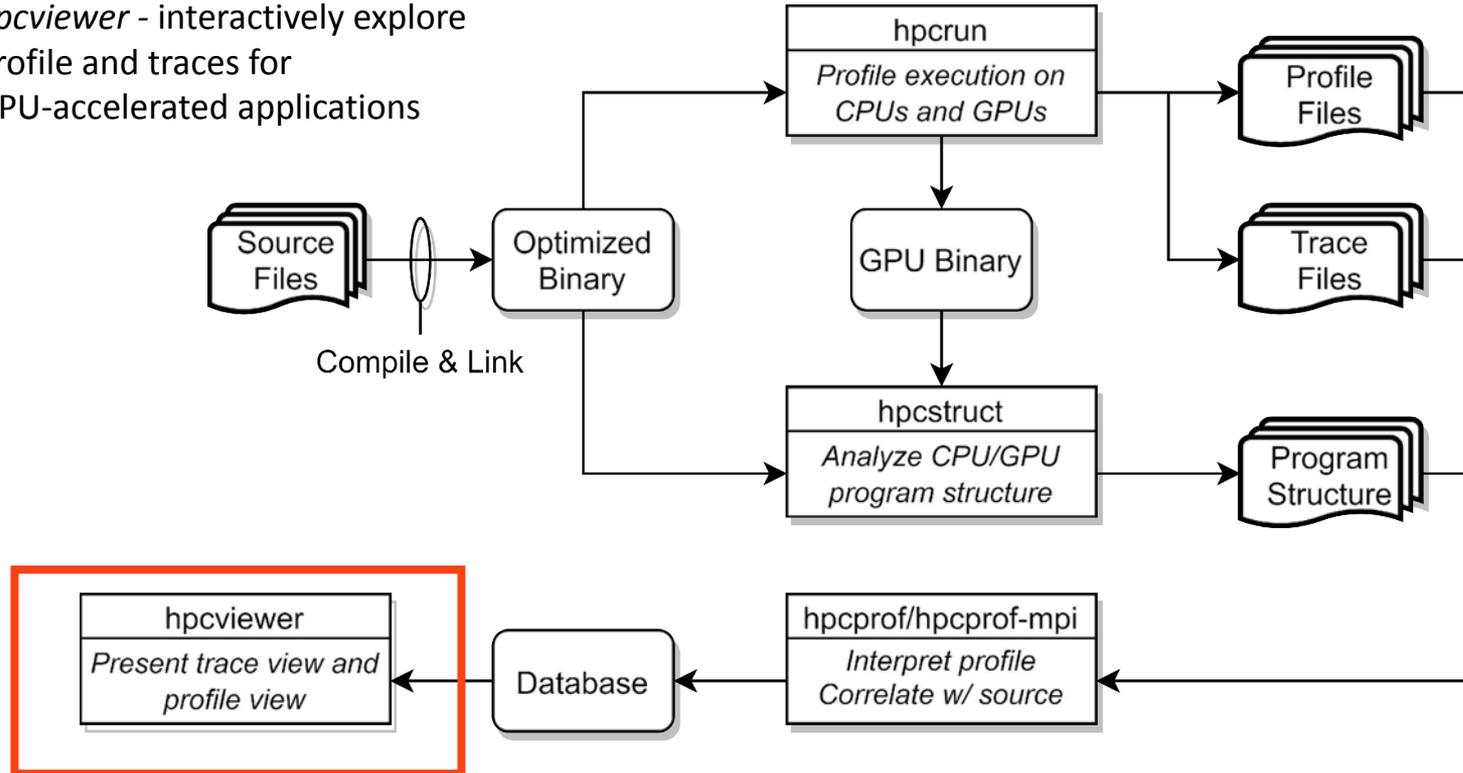
- Analyze data from large-scale executions with distributed-memory parallelism + multithreading

```
srun -n ${NODES} --ppn 1 -depth=128 \  
hpcprof-mpi <measurement-directory>
```

HPCToolkit's Workflow for GPU-accelerated Applications

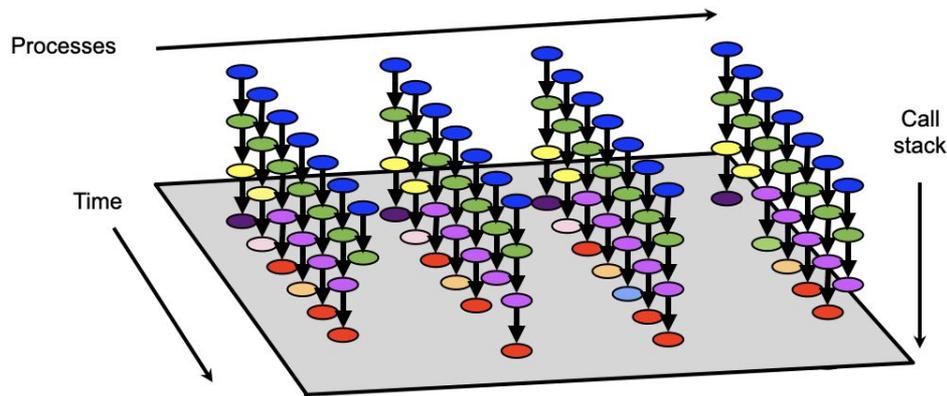
Step 4:

- *hpcviewer* - interactively explore profile and traces for GPU-accelerated applications



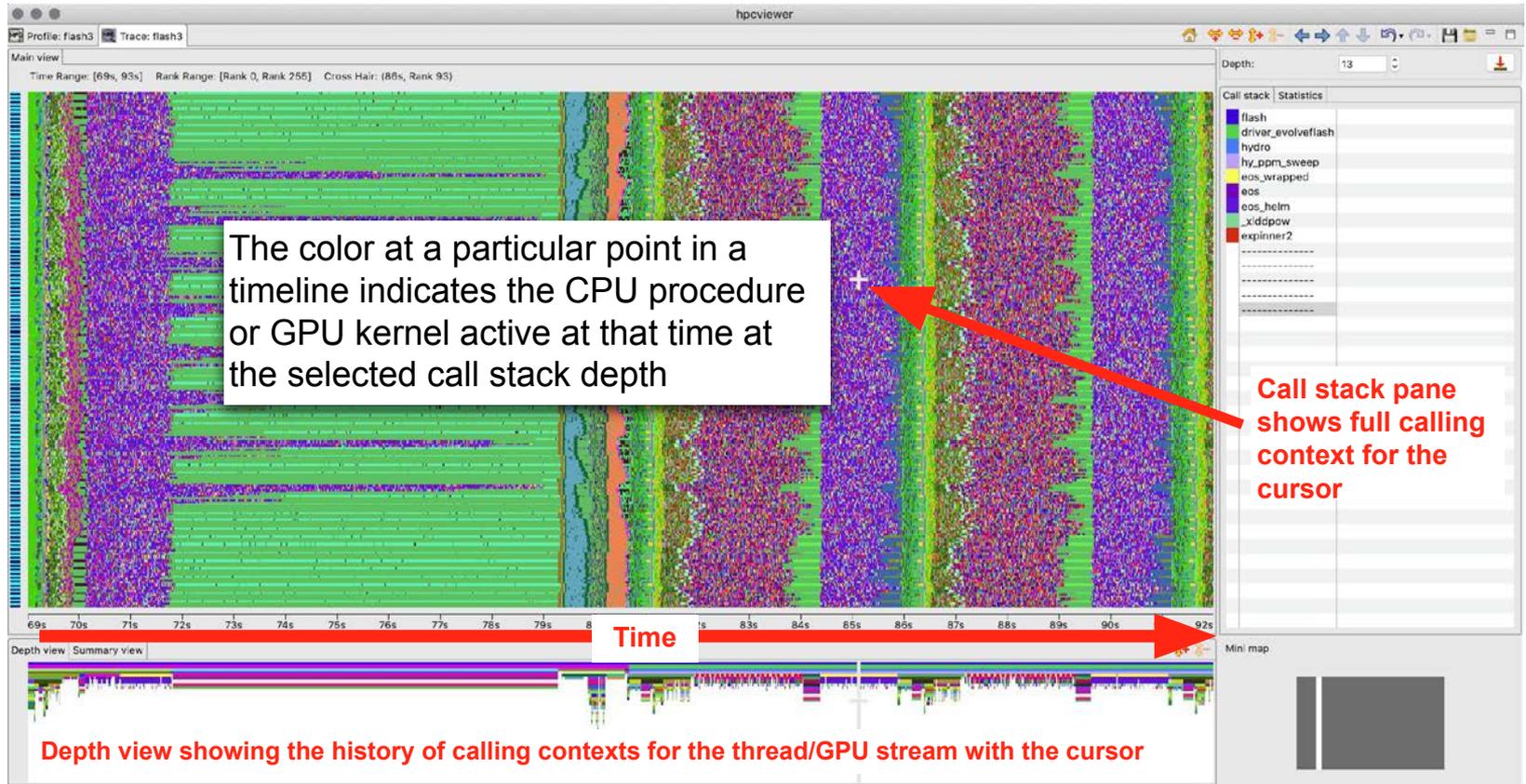
Understanding Temporal Behavior

- Profiling compresses out the temporal dimension
 - Temporal patterns, e.g. serial sections and dynamic load imbalance are invisible in profiles
- What can we do? Trace call path samples
 - N times per second, take a call path sample of each thread
 - Organize the samples for each thread along a time line
 - View how the execution evolves left to right
 - What do we view? assign each procedure a color; view a depth slice of an execution



Understanding hpcviewer's Trace View

MPI ranks, OpenMP Threads, GPU streams



Today's Agenda

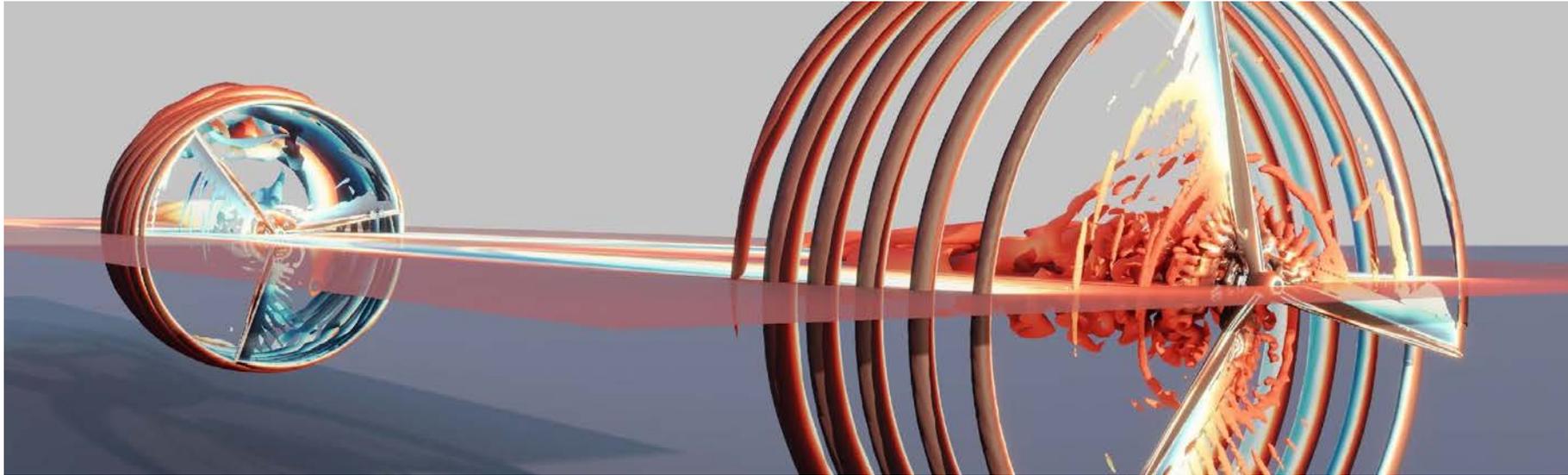
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Case Studies

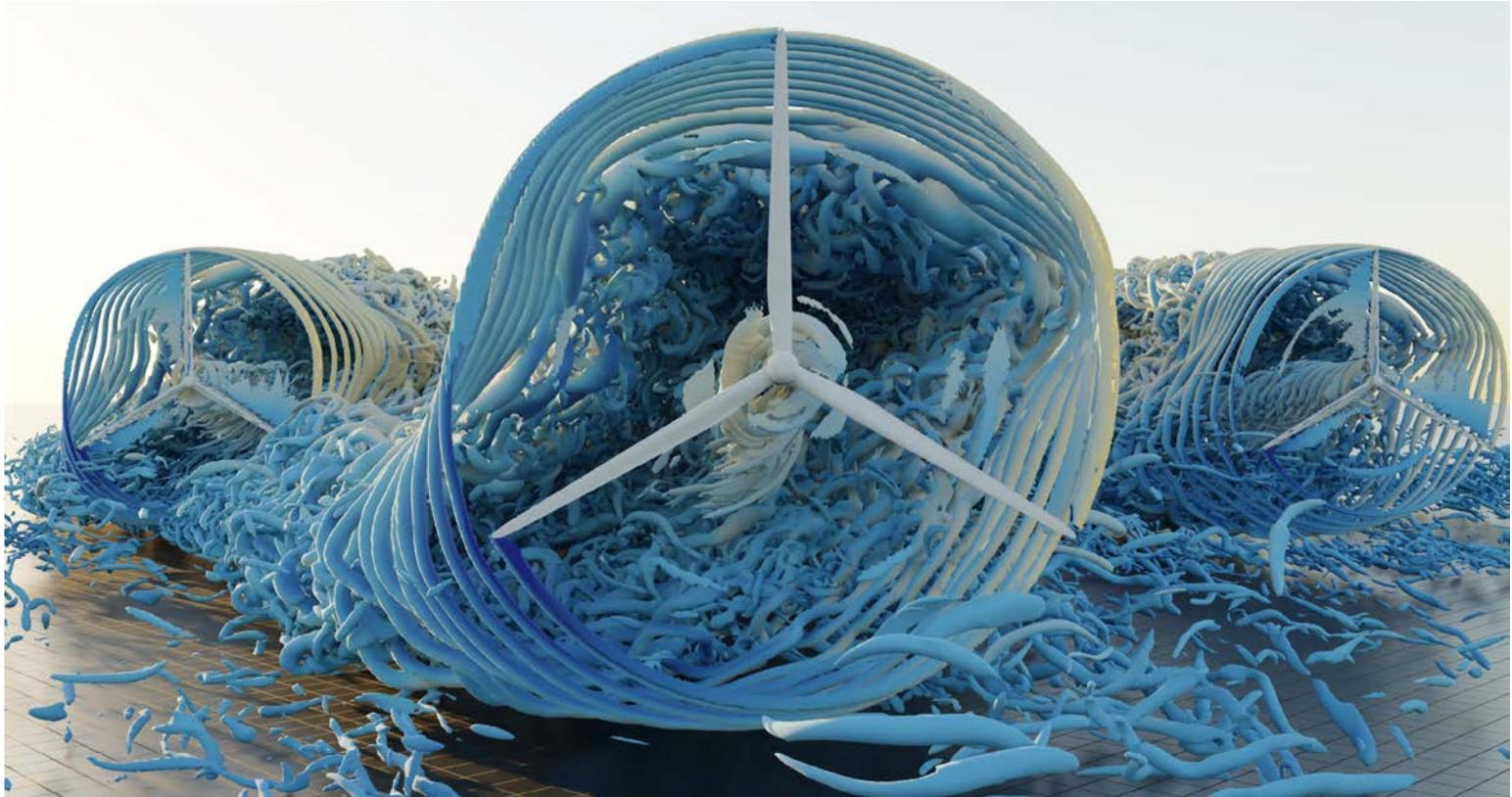
- ExaWind (Nalu-Wind + AMRWind) - Wind turbine and wind farm simulation
- PeleLMeX - Adaptive mesh hydrodynamics code for low mach number reacting flows
- GAMESS (OpenMP) - general ab initio quantum chemistry package
- LAMMPS (Kokkos) - classical molecular dynamics code with a focus on materials modeling

At Exascale!

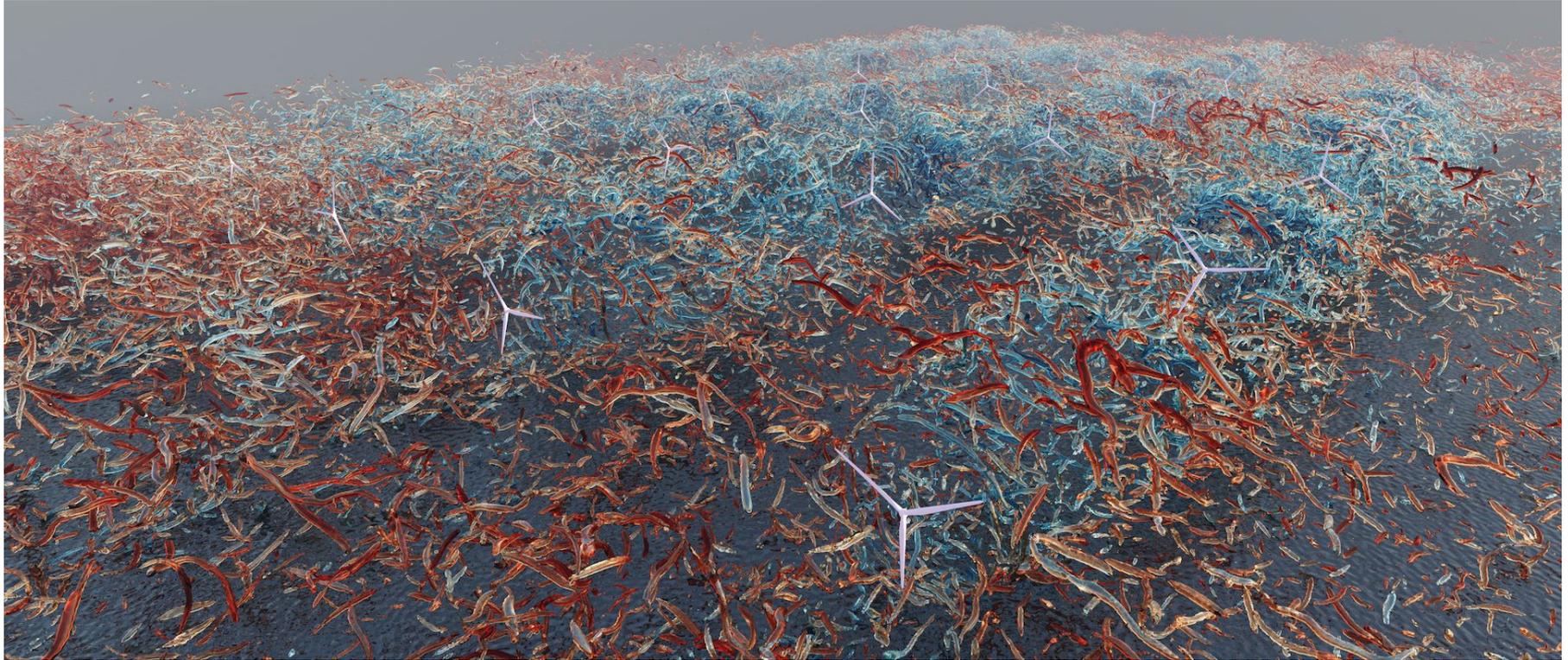
ExaWind: Modeling Turbine Wake Formation



ExaWind: Wakes from Three Turbines over Time



ExaWind: Visualization of a Wind Farm Simulation



ExaWind: Execution Traces on Frontier Collected with HPCToolkit

Traces on roughly 64K MPI ranks + 8K GPUs for ~17minutes

Before: MPI waiting (bad), shown in red

After: MPI overhead negligible*

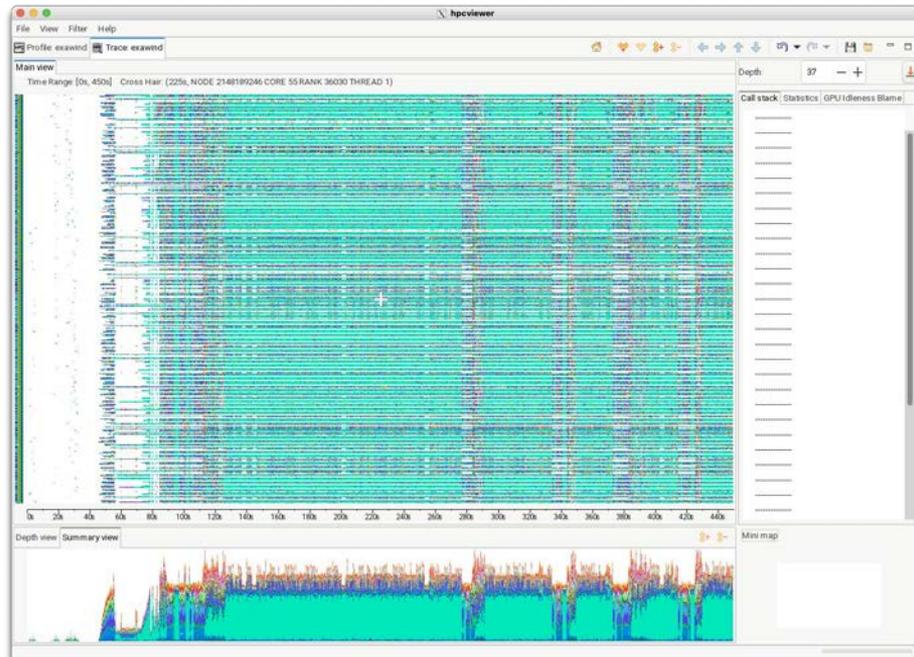
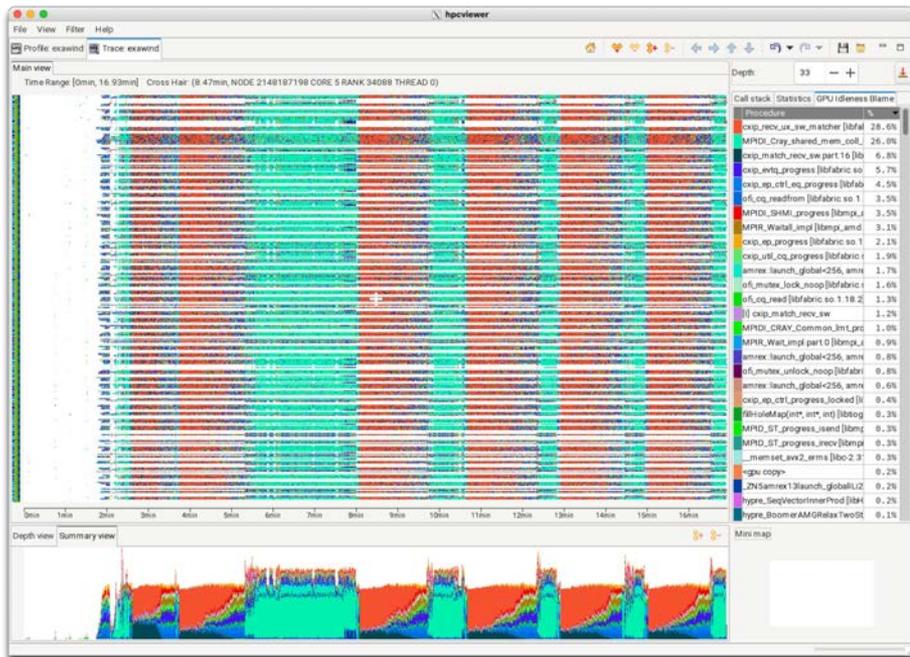


Figure credits: Jon Rood, NREL

*replaced non-blocking send/recv with ialltoallv

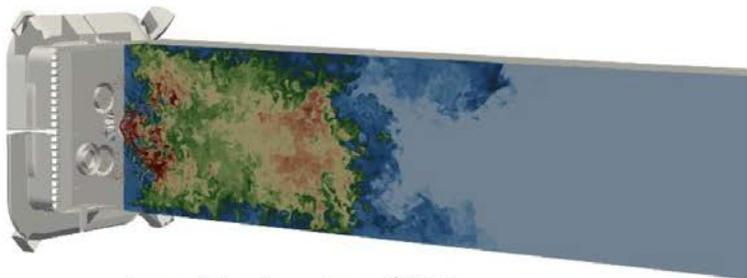
ExaWind Testimonials for HPCToolkit

I just wanted to mention we've been using HPCToolkit a lot for our ExaWind application on Frontier, which is a hugely complicated code, and your profiler is one of the only ones we've found that really lets us easily instrument and then browse what our application is doing at runtime including GPUs. As an example, during a recent hackathon we had, we improved our large scale performance by 24x by understanding our code better with HPCToolkit and running it on 1000s of nodes while profiling. We also recently improved upon this by 10% for our total runtime.

- Jon Rood NREL (5/31/2024)

One big thing for us is that we can't overstate how complicated ExaWind is in general, and how complicated it is to build, so finding out that HPCToolkit could easily profile our entire application without a ton of instrumentation during the build process, and be able to profile it on a huge amount of Frontier with line numbers and visualizing the trace was really amazing to us.

- Jon Rood NREL (6/3/2024)



Load balancing OFF
(pink-> Cvode calls)

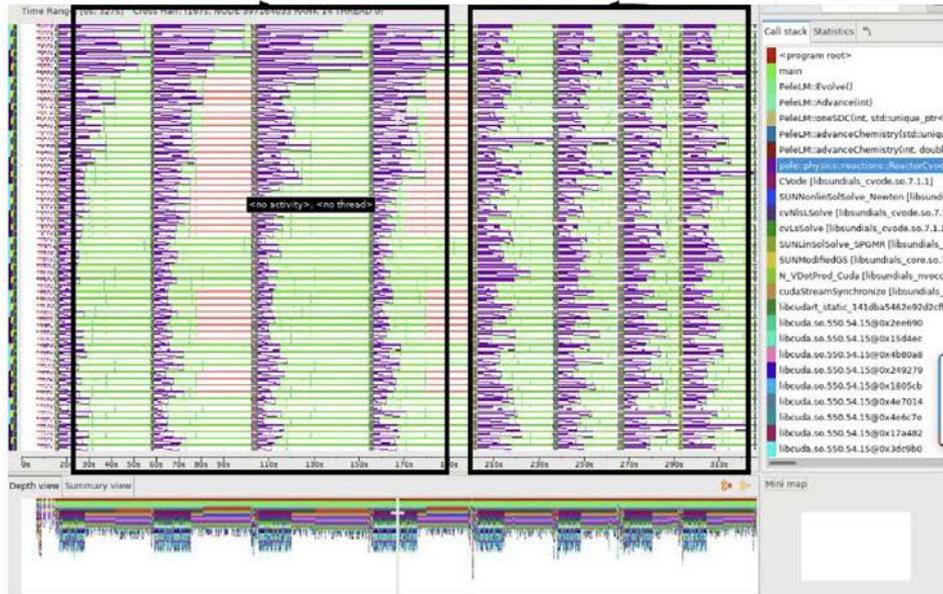
Full SAF Case: Load Balancing Issues

Confirmed on Frontier
production runs:
60% speedup

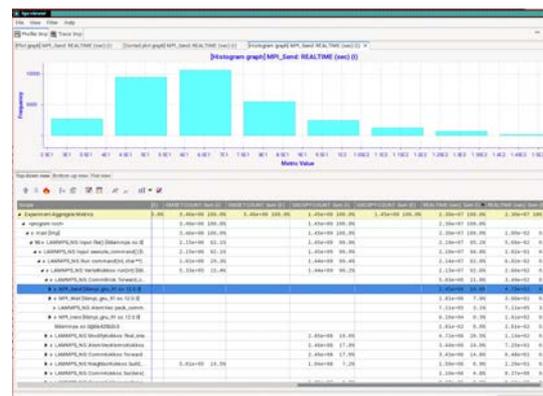
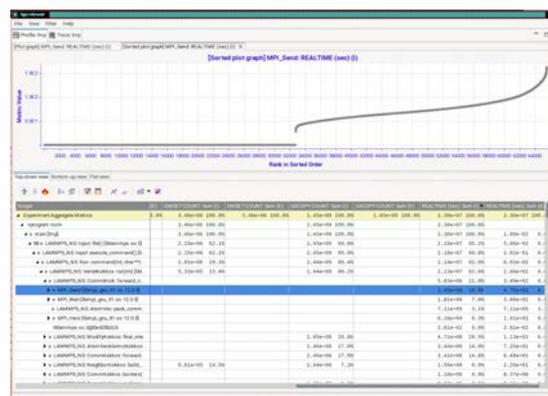
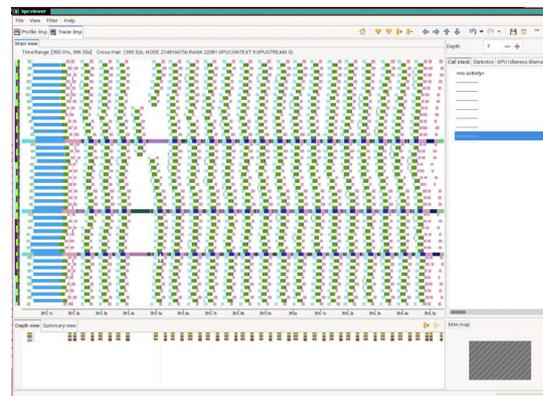
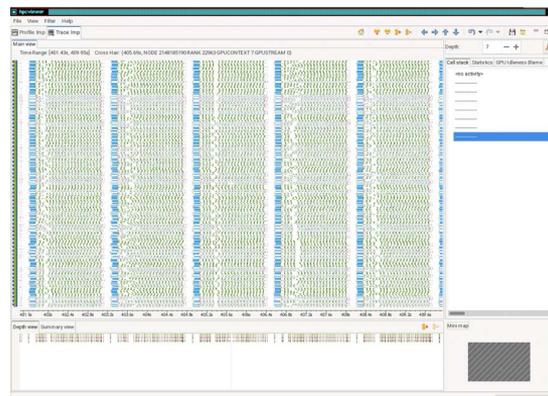
Load balancing ON

Grid from checkpoint file
before regridding, Avg.
Time/dt = 92.3 s

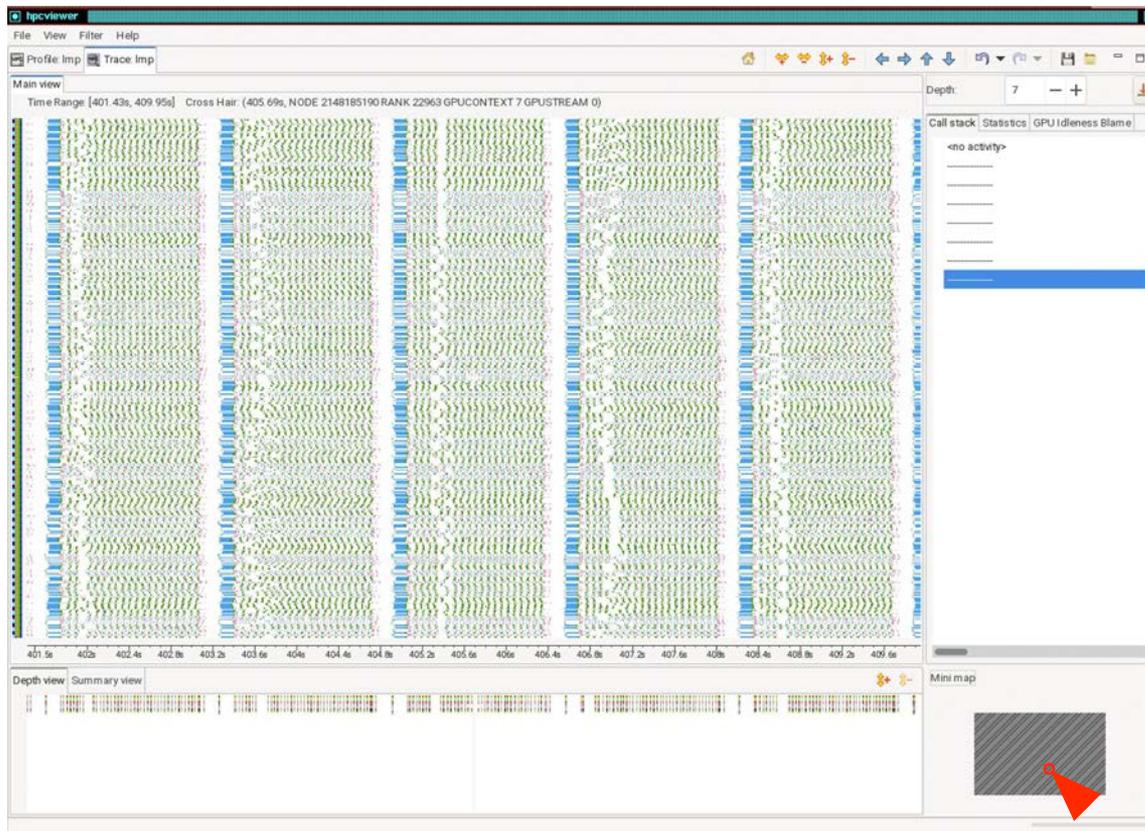
After regridding, Avg. Time/dt = 62.5s
- Down to 40s when setting
amr.max_grid_size=32



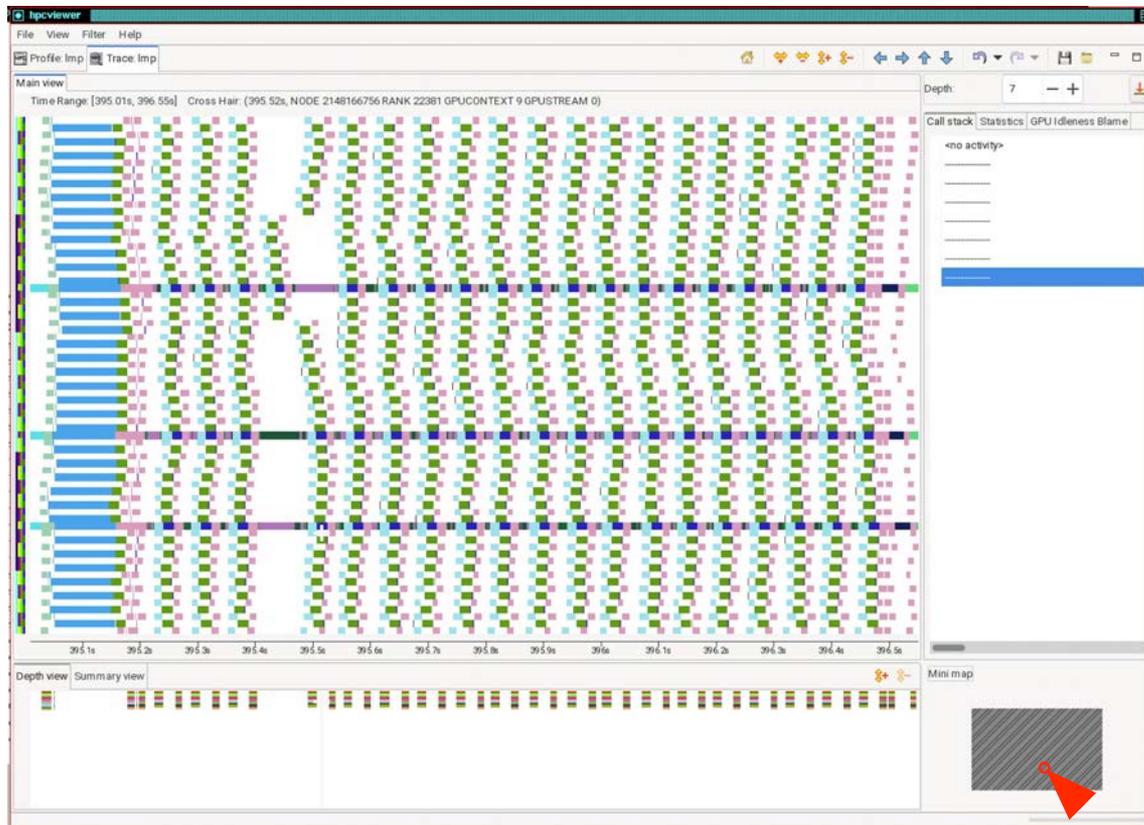
LAMMPS on Frontier: Executions with Kernel Duration of Milliseconds



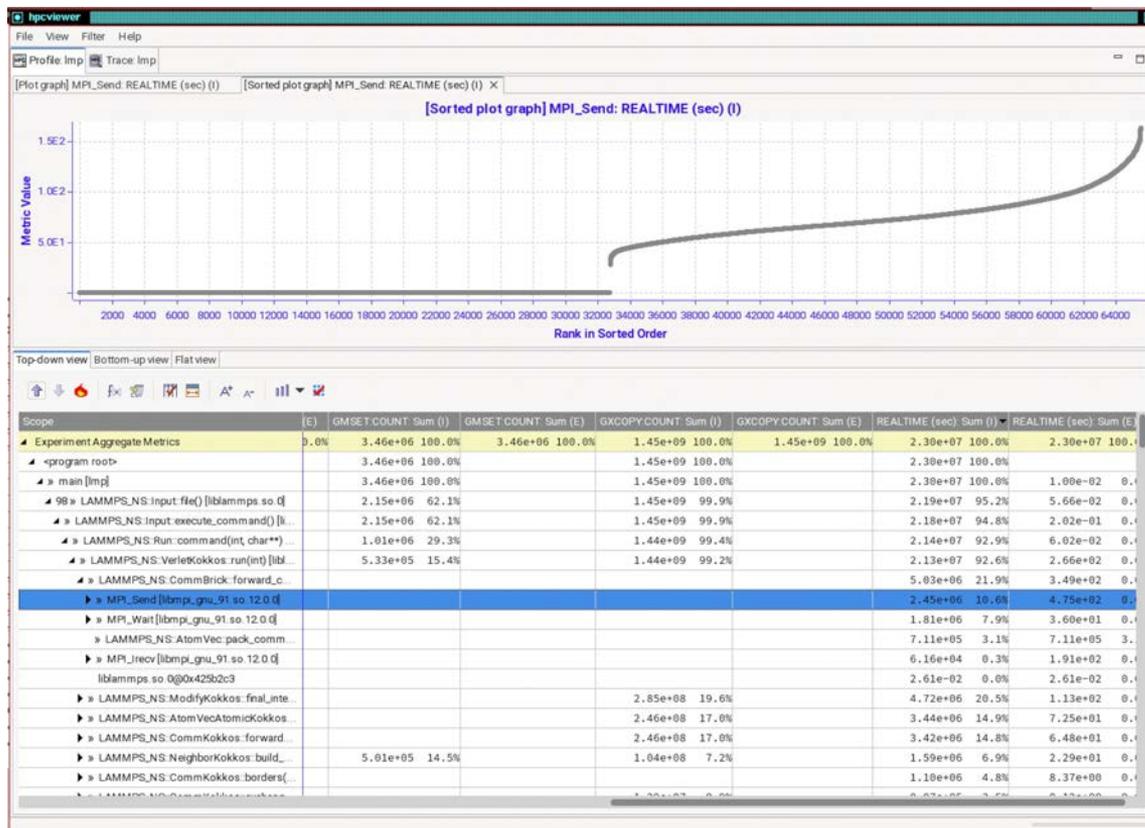
LAMMPS on Frontier: Executions with Kernel Duration of Milliseconds



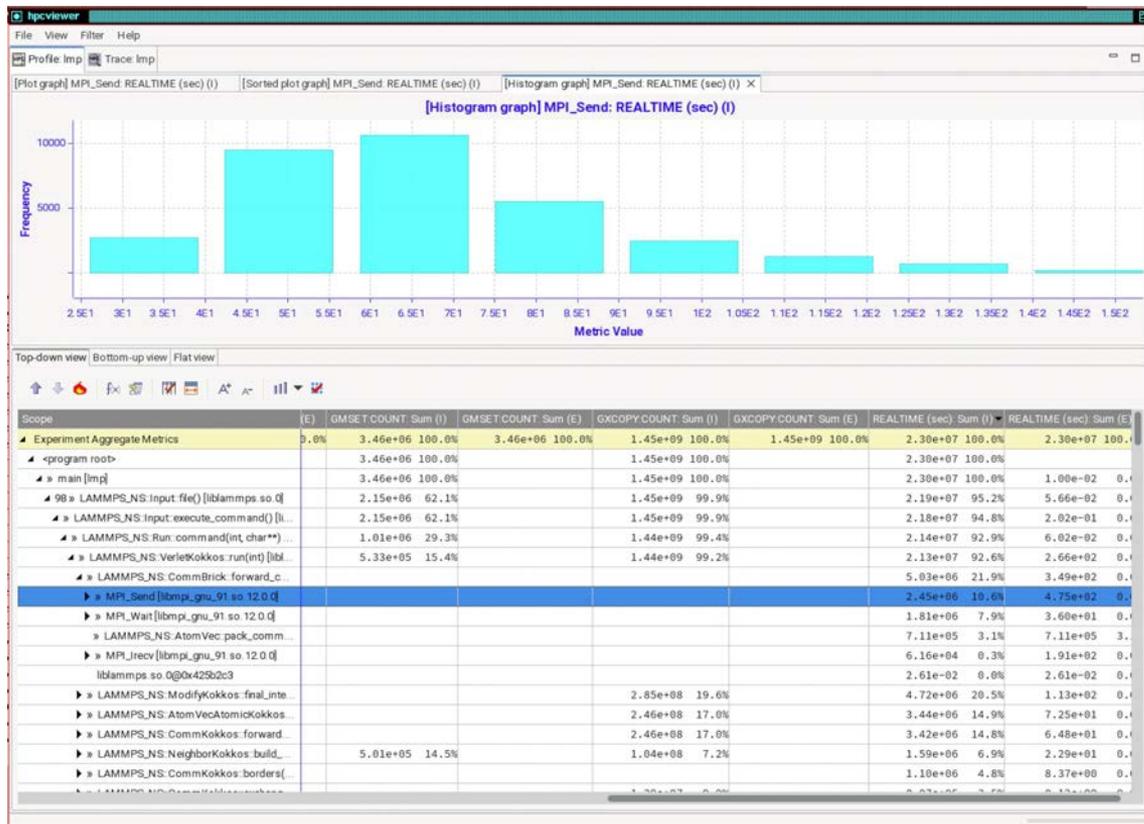
LAMMPS on Frontier: Executions with Kernel Duration of Milliseconds



LAMMPS on Frontier: Executions with Kernel Duration of Milliseconds

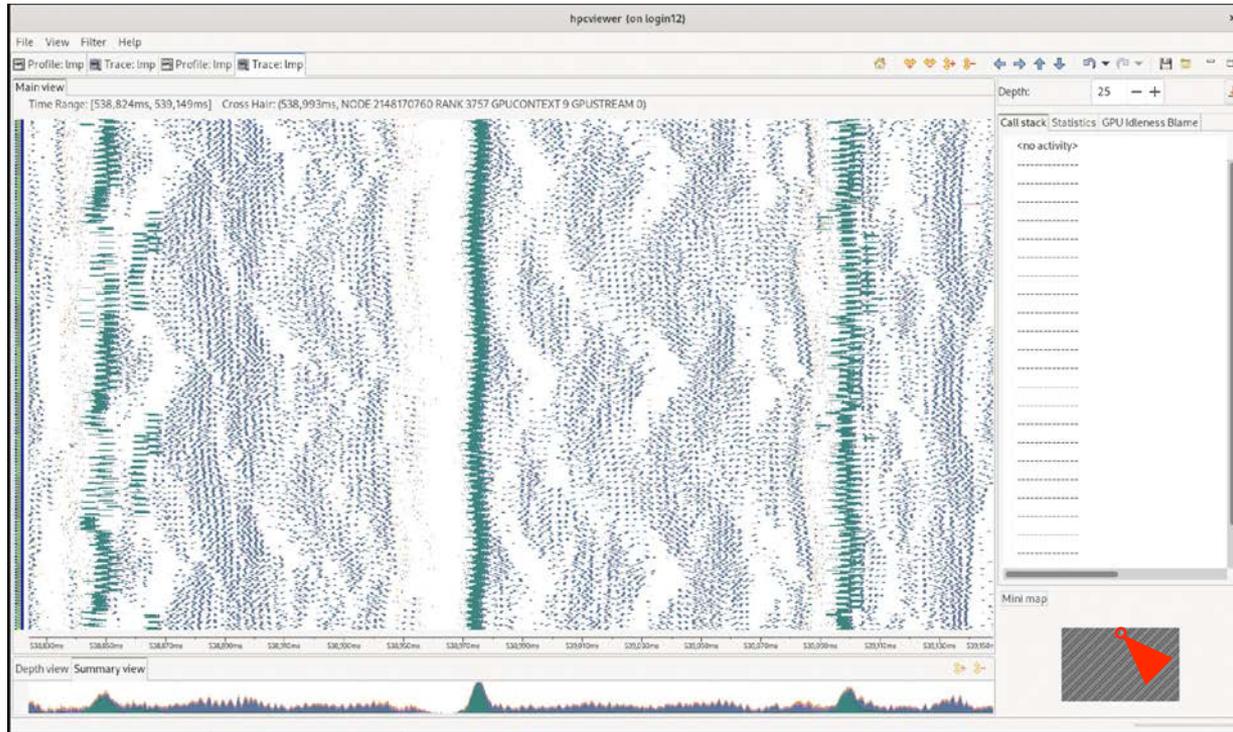


LAMMPS on Frontier: Executions with Kernel Duration of Milliseconds



LAMMPS on Frontier: 8K nodes, 64K MPI ranks + 64K GPU tiles

Kernel duration of microseconds



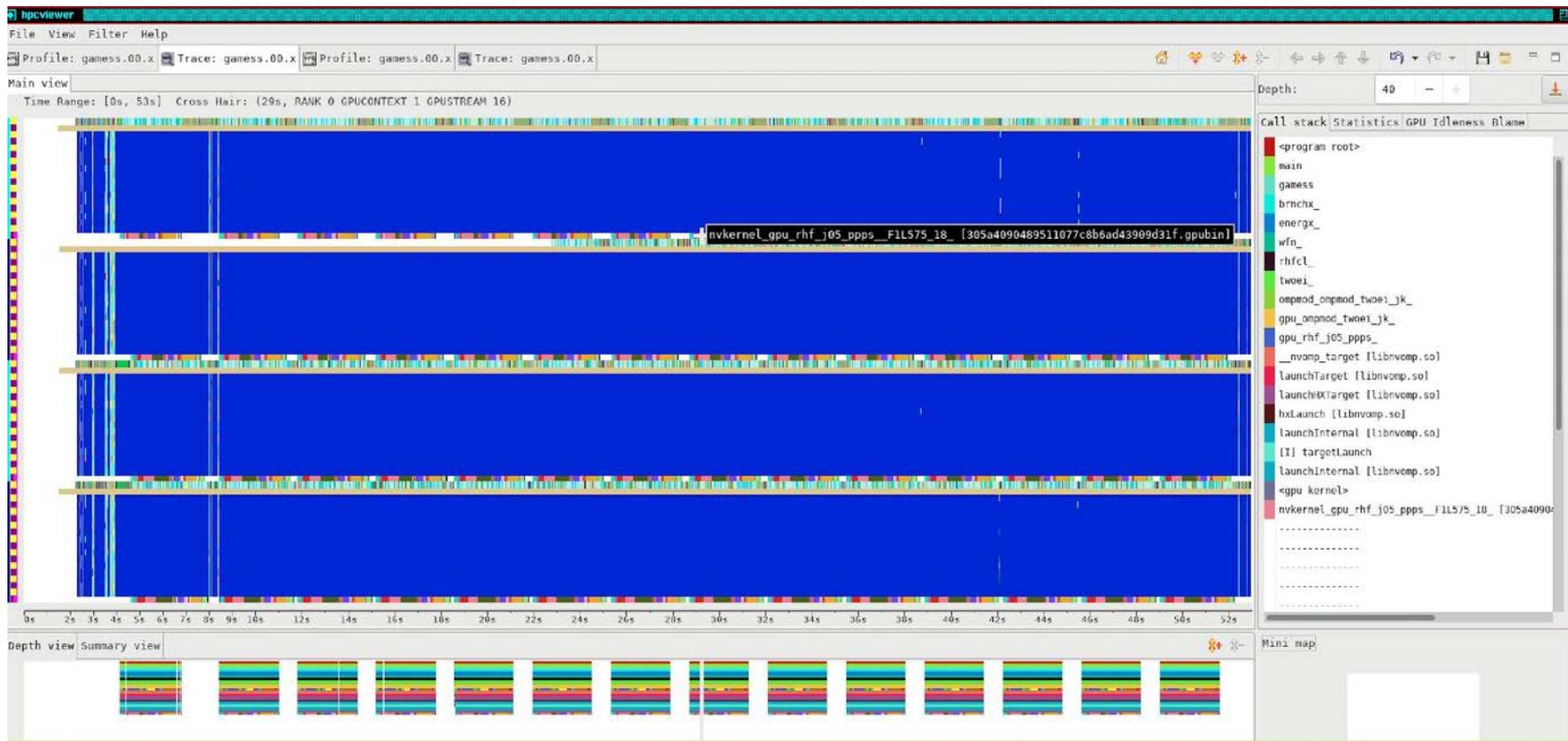
Case Study: GAMESS

- General Atomic and Molecular Electronic Structure System (GAMESS)
 - general *ab initio* quantum chemistry package
- Calculates the energies, structures, and properties of a wide range of chemical systems
- Experiments
 - GPU-accelerated nodes at a prior Perlmutter hackathon
 - Single node with 4 GPUs
 - Five nodes with 20 GPUs

Perlmutter node at a glance

AMD Milan CPU
4 NVIDIA A100 GPUs
256 GB memory

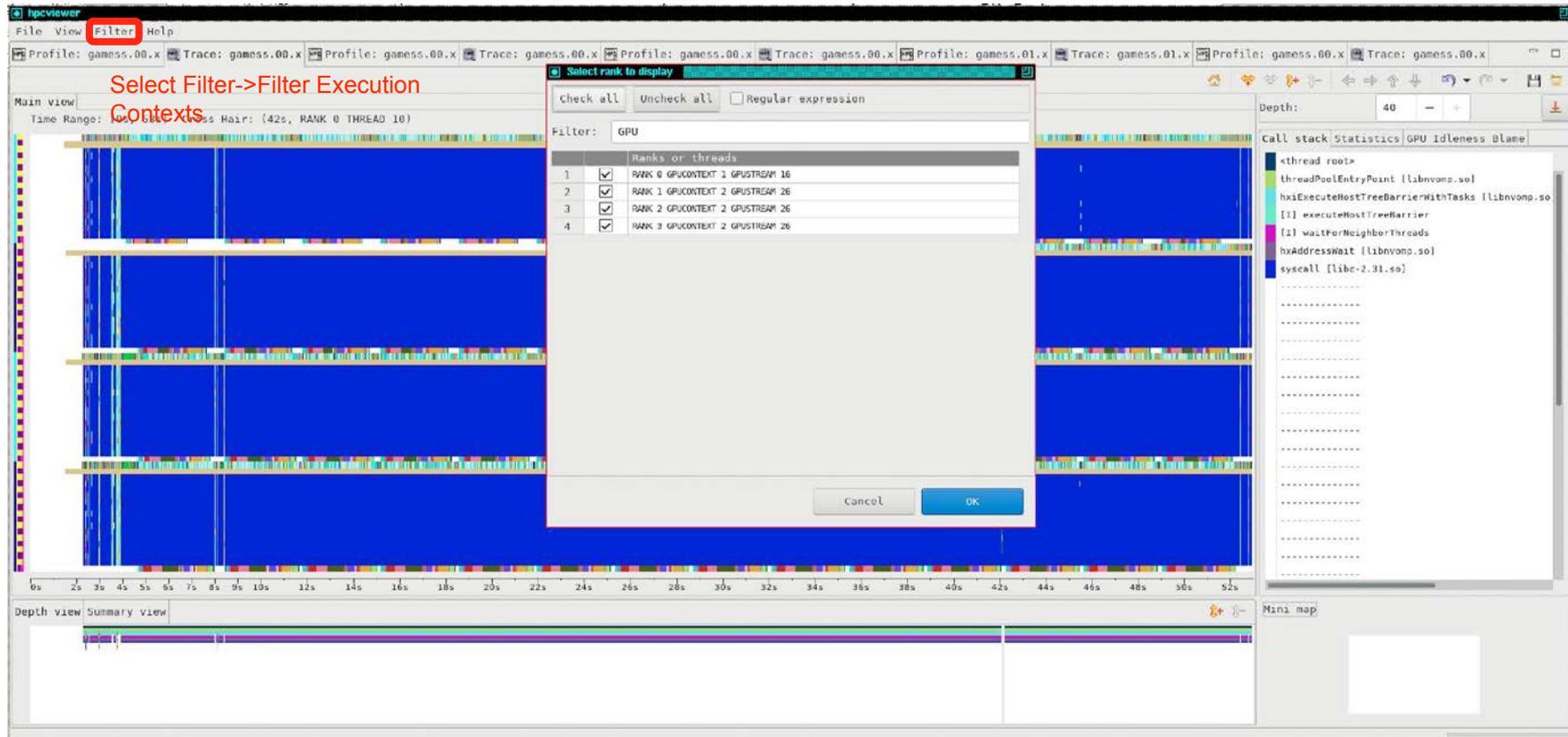
Time-centric Analysis: GAMESS 4 ranks, 4 GPUs on Perlmutter



GAMESS original

All CPU threads and GPU streams

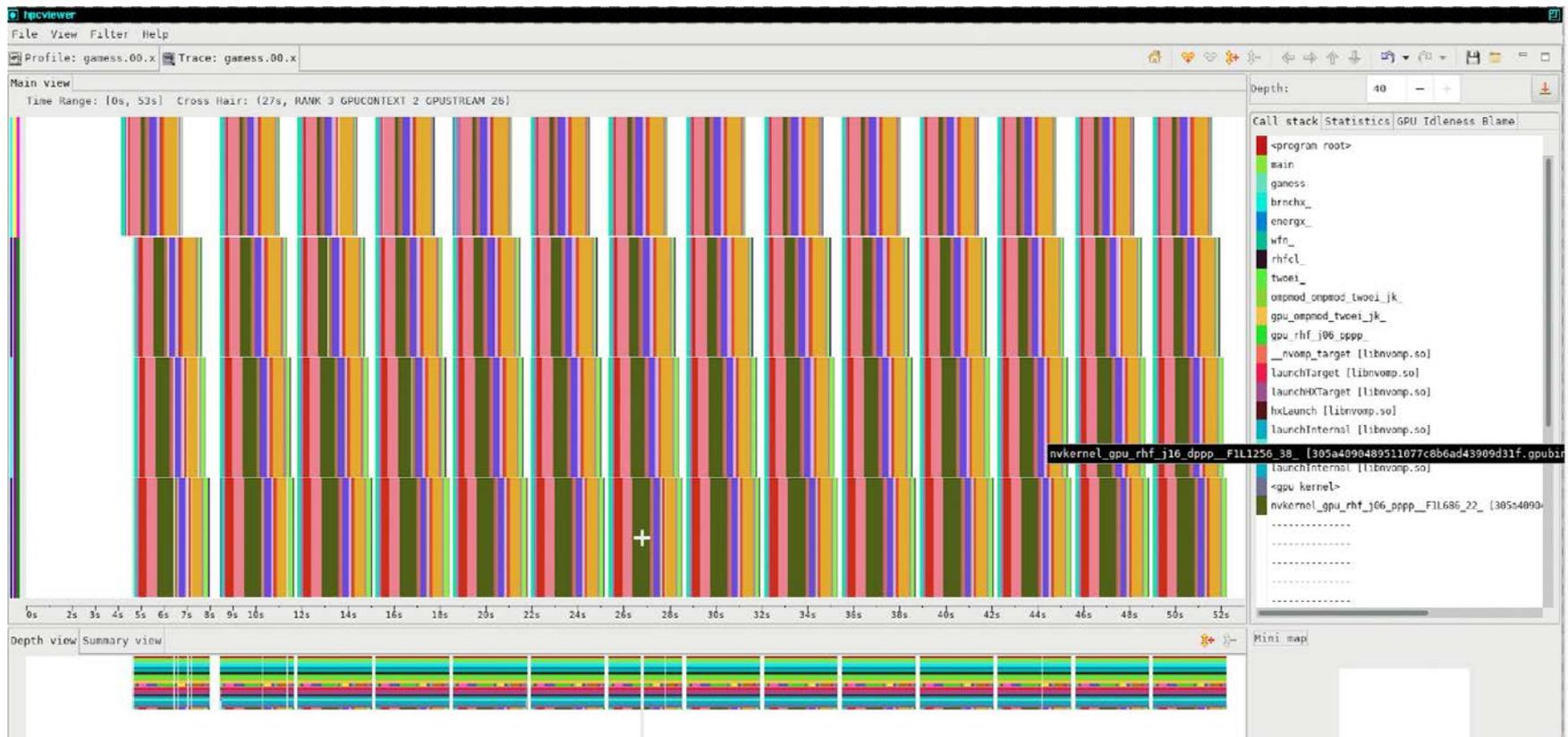
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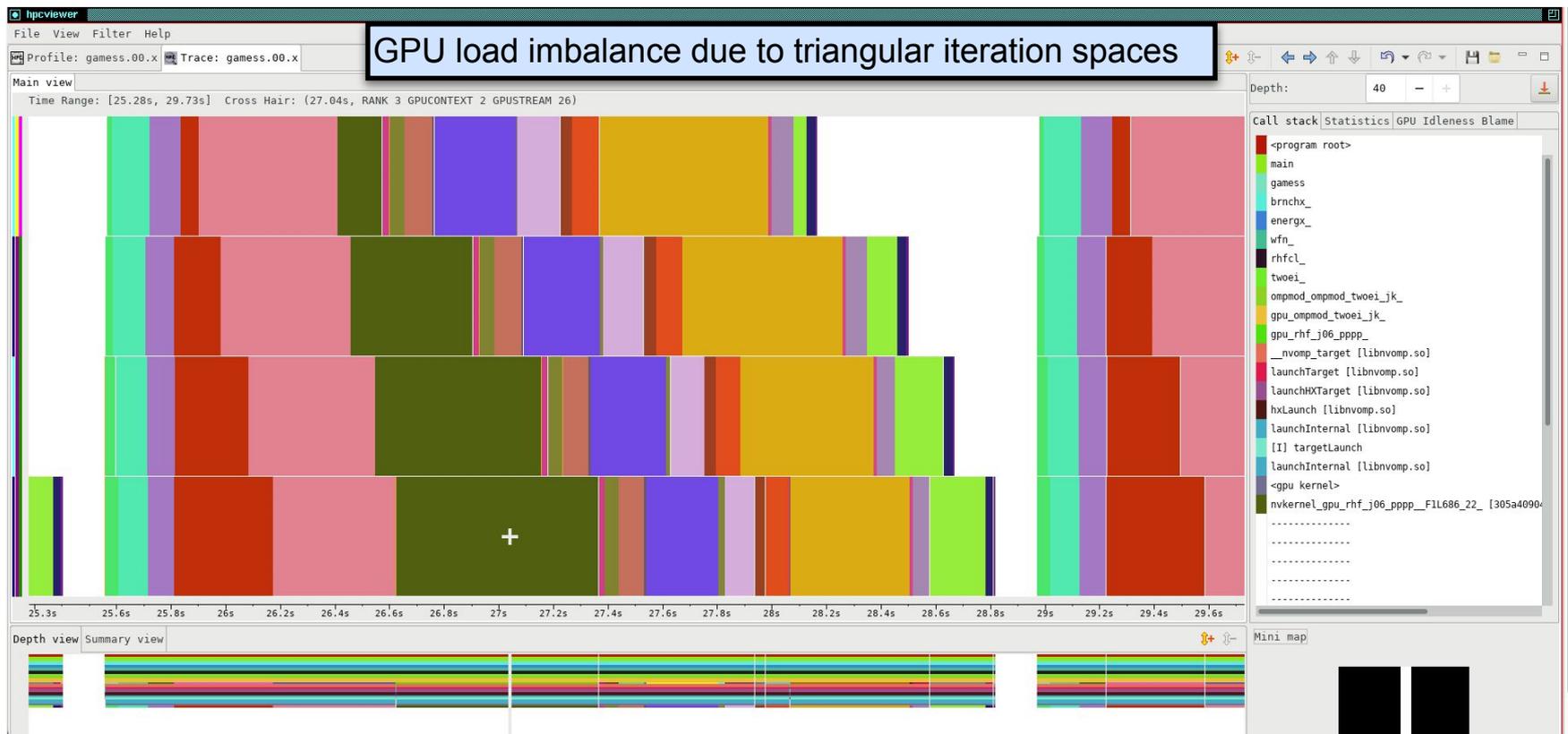
Time-centric Analysis: GAMESS 4 ranks, 4 GPUs on Perlmutter



GAMESS original

All GPU streams; whole execution

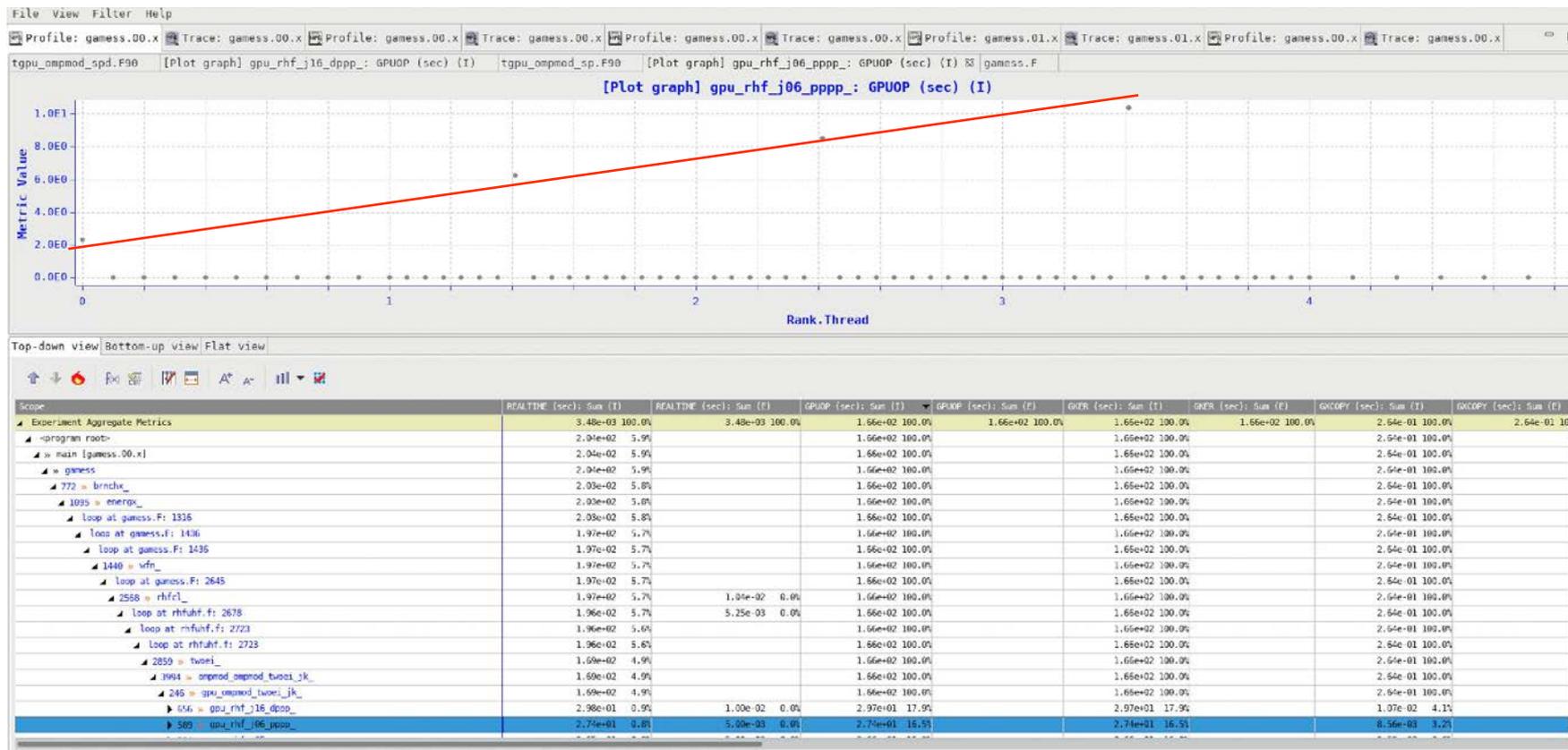
Time-centric Analysis: GAMESS 4 ranks, 4 GPUs on Perlmutter



GAMESS original

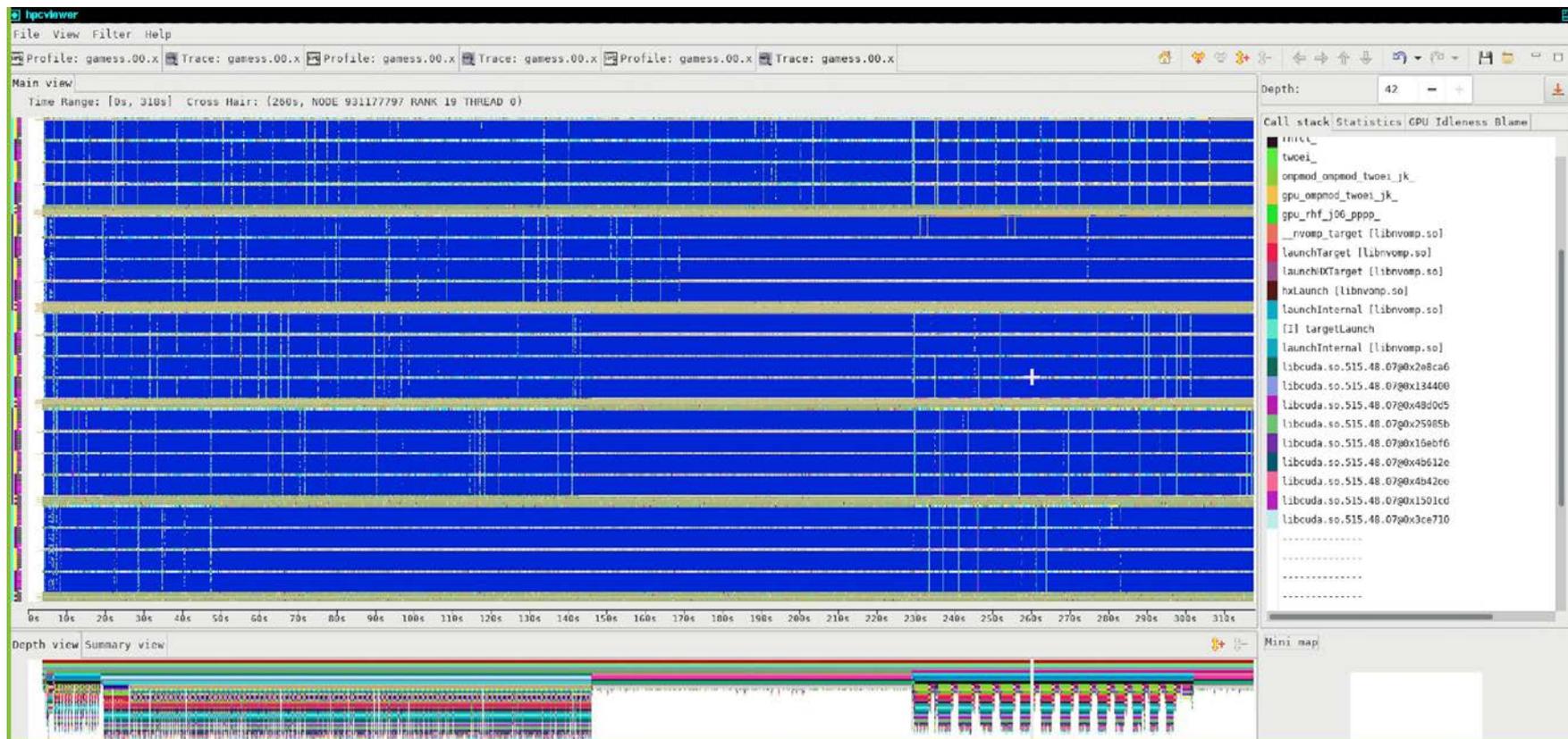
GPU streams: 1 iteration

Time-centric Analysis: GAMESS 4 ranks, 4 GPUs on Perlmutter



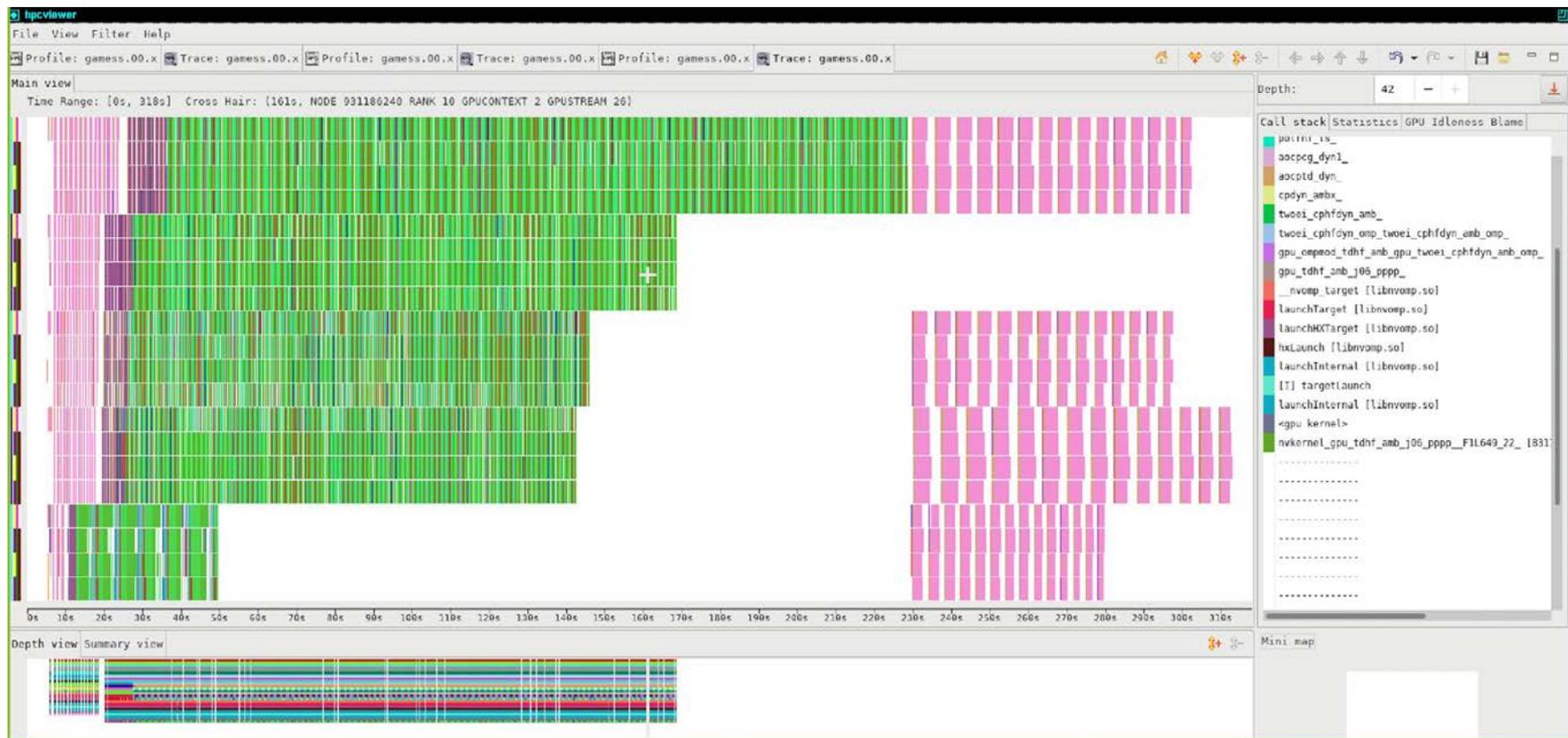
GAMESS original

Time-centric Analysis: GAMESS **5 nodes, 40 ranks, 20 GPUs** on Perlmutter

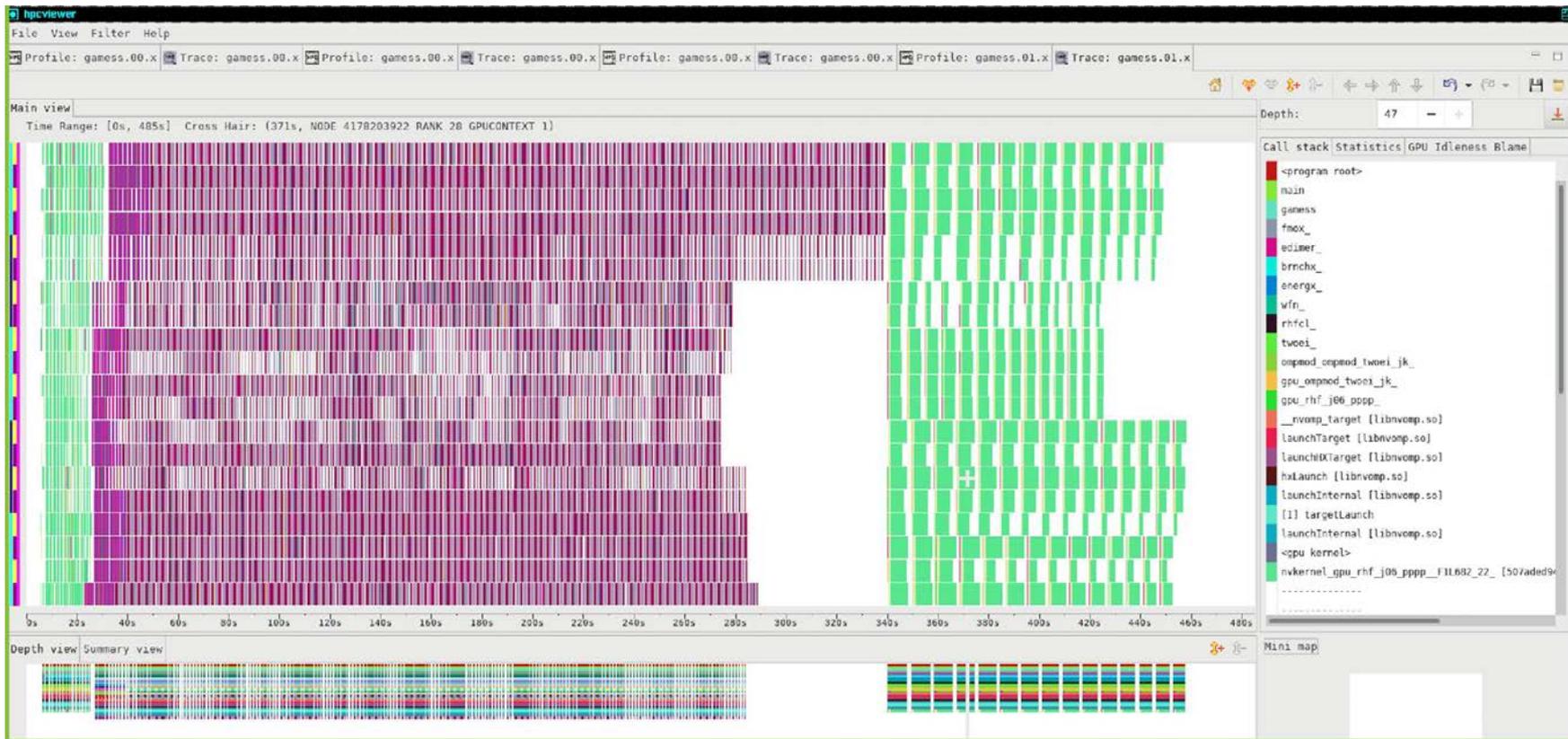


CPU Threads and GPU Streams

Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter

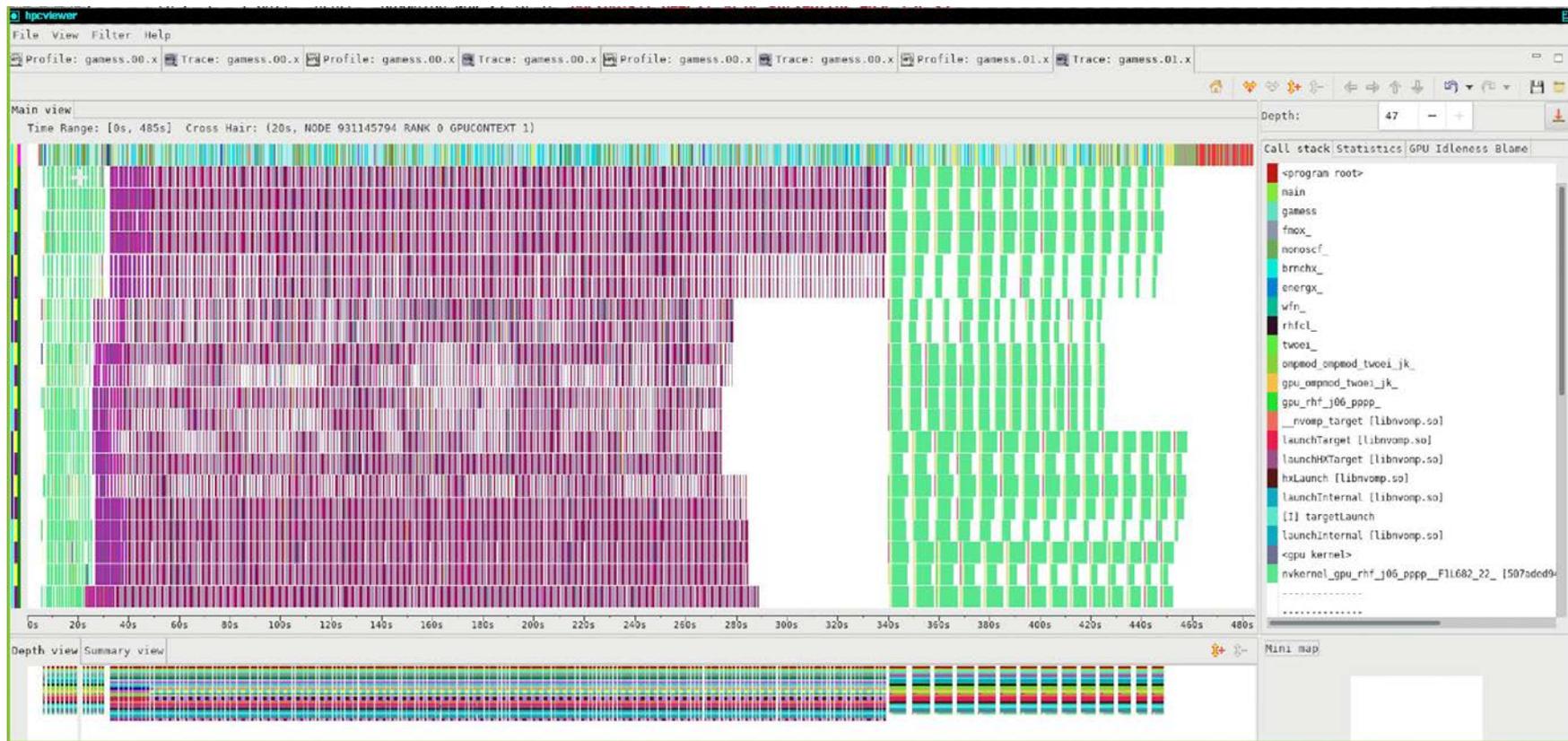


Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter



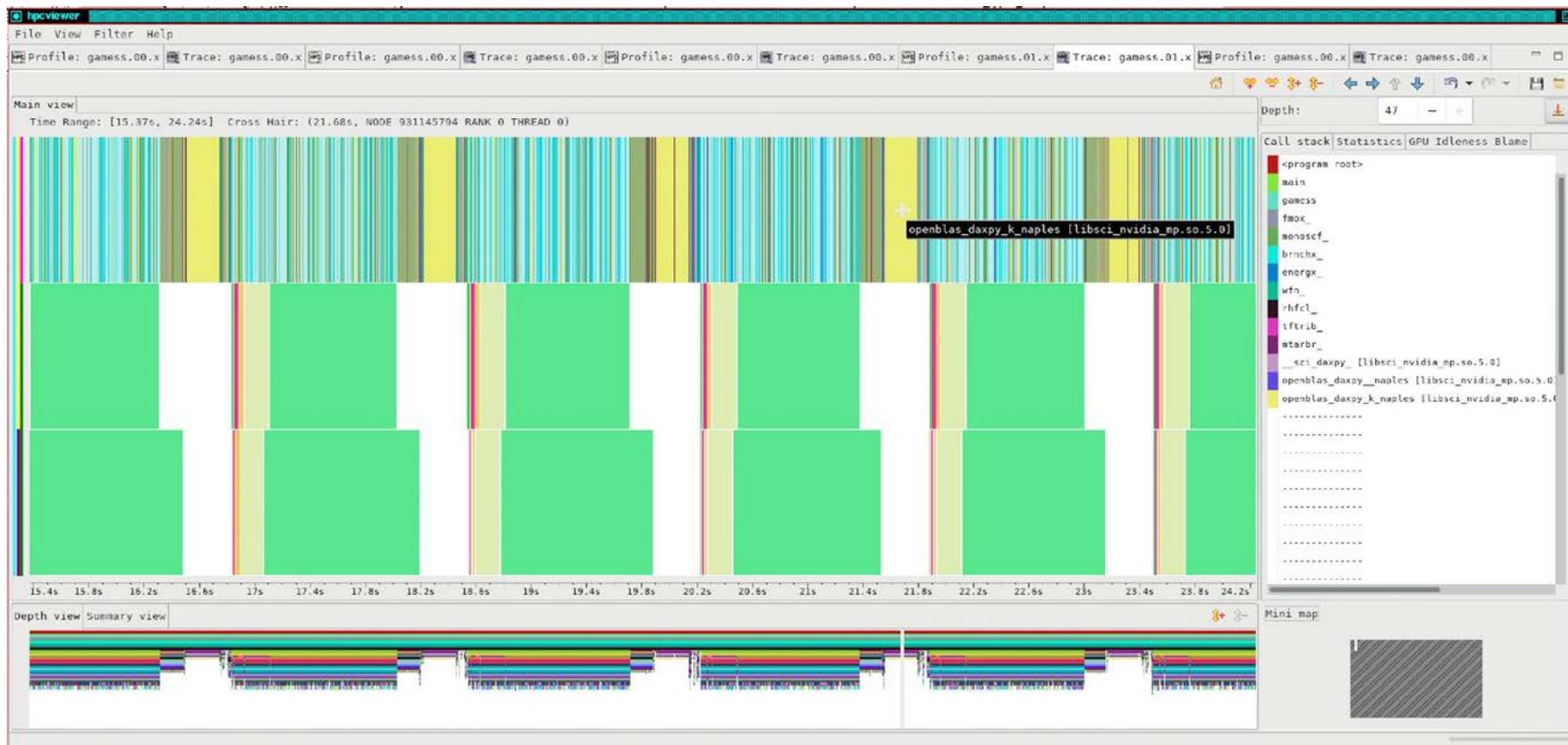
GAMESS improved with better manual distribution of work in input

Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter



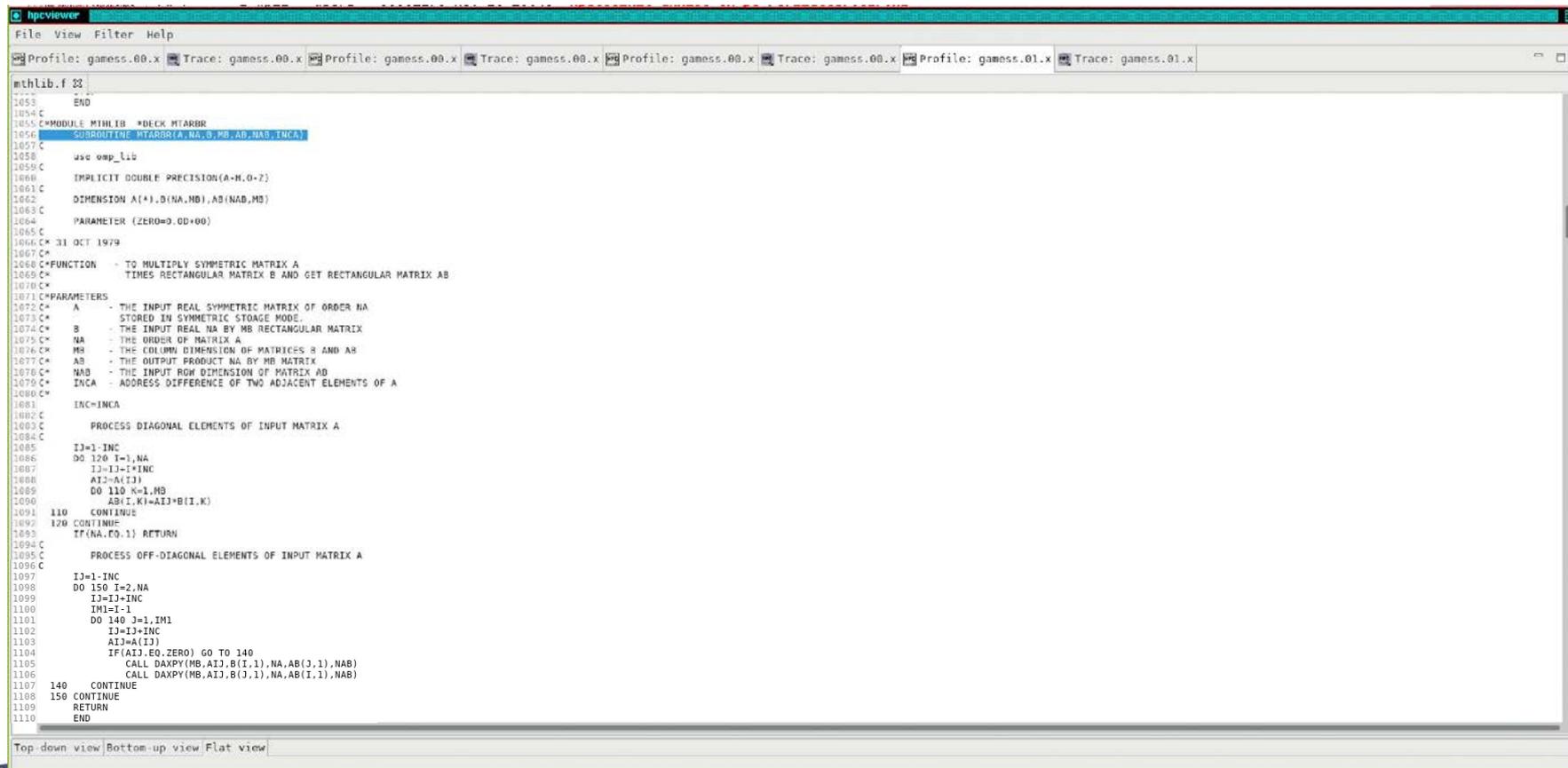
GAMESS improved adding Rank 0 Thread 0 to GPU streams

Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter



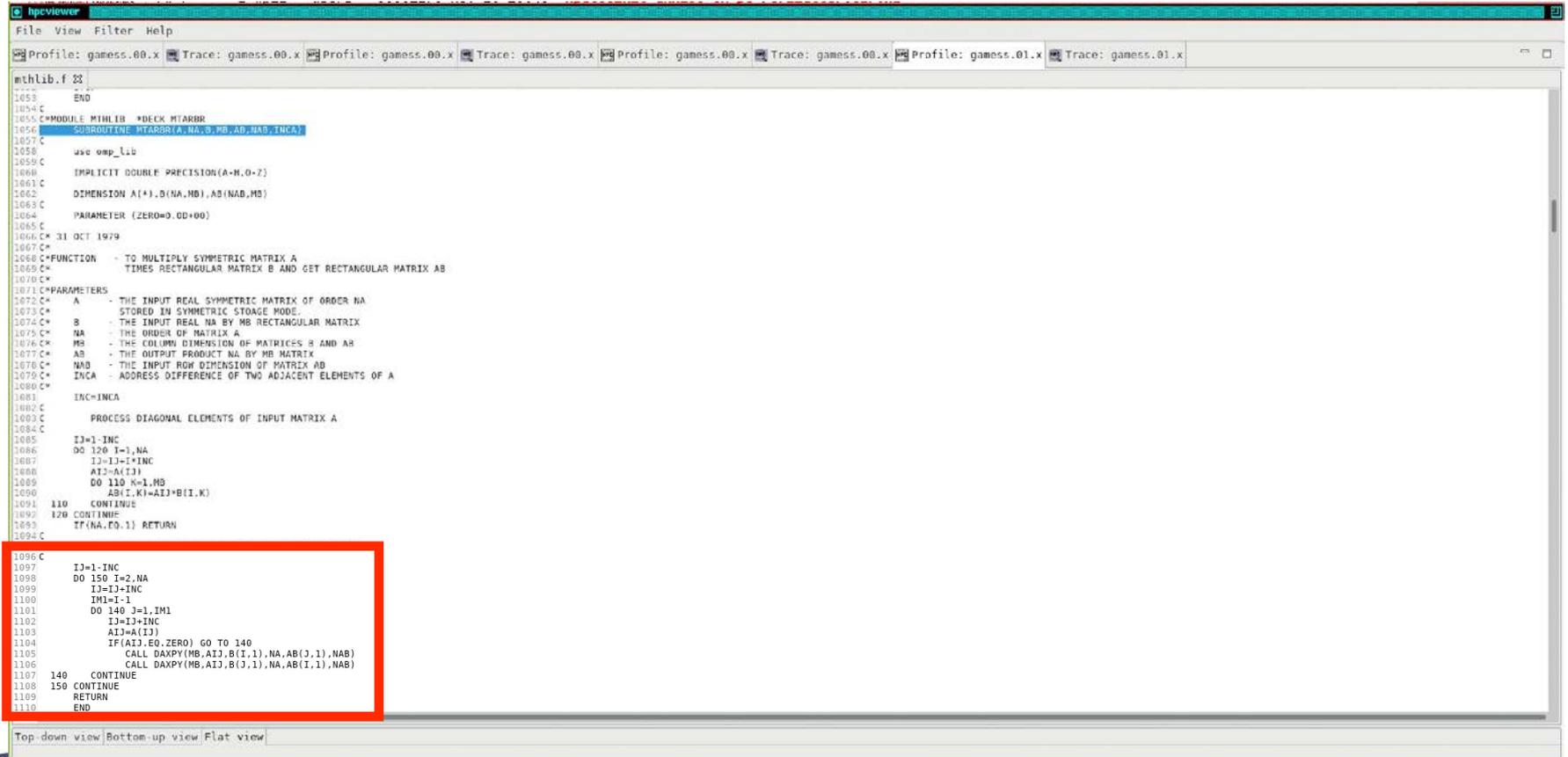
1 CPU Stream, 2 GPU Streams: 6 Iterations

Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter



```
hpcviewer
File View Filter Help
Profile: game... Trace: game... Profile: game... Trace: game... Profile: game... Trace: game... Profile: game... Trace: game...
mthlib.f 33
1053 C
1054 C
1055 C*MODULE MTHLIB *DECK MTARRB
1056 C SUBROUTINE MTARRB(A,NA,MB,AB,INCA,INCA)
1057 C
1058 C use omp_lib
1059 C
1060 C IMPLICIT DOUBLE PRECISION(A-H,O-Z)
1061 C
1062 C DIMENSION A(*),B(NA,MB),AB(INAB,MB)
1063 C
1064 C PARAMETER (ZERO=0.0D+00)
1065 C
1066 C* 31 OCT 1979
1067 C*
1068 C*FUNCTION - TO MULTIPLY SYMMETRIC MATRIX A
1069 C* TIMES RECTANGULAR MATRIX B AND GET RECTANGULAR MATRIX AB
1070 C*
1071 C*PARAMETERS
1072 C* A - THE INPUT REAL SYMMETRIC MATRIX OF ORDER NA
1073 C* STORED IN SYMMETRIC STORAGE MODE.
1074 C* B - THE INPUT REAL NA BY MB RECTANGULAR MATRIX
1075 C* NA - THE ORDER OF MATRIX A
1076 C* MB - THE COLUMN DIMENSION OF MATRICES B AND AB
1077 C* AB - THE OUTPUT PRODUCT NA BY MB MATRIX
1078 C* NAB - THE INPUT ROW DIMENSION OF MATRIX AB
1079 C* INCA - ADDRESS DIFFERENCE OF TWO ADJACENT ELEMENTS OF A
1080 C*
1081 C INCA=INCA
1082 C
1083 C PROCESS DIAGONAL ELEMENTS OF INPUT MATRIX A
1084 C
1085 C IJ=1-INC
1086 C DO 120 I=1,NA
1087 C IJ=IJ+INC
1088 C AIJ=A(IJ)
1089 C DO 110 N=1,MB
1090 C AB(I,K)=AIJ*B(I,K)
1091 C 110 CONTINUE
1092 C 120 CONTINUE
1093 C IF(NA.EQ.1) RETURN
1094 C
1095 C PROCESS OFF-DIAGONAL ELEMENTS OF INPUT MATRIX A
1096 C
1097 C IJ=1-INC
1098 C DO 150 I=2,NA
1099 C IJ=IJ+INC
1100 C IMI=I-1
1101 C DO 140 J=1,IMI
1102 C IJ=IJ+INC
1103 C AIJ=A(IJ)
1104 C IF(AIJ.EQ.ZERO) GO TO 140
1105 C CALL DAXPY(MB,AIJ,B(I,1),NA,AB(J,1),NAB)
1106 C CALL DAXPY(MB,AIJ,B(J,1),NA,AB(I,1),NAB)
1107 C 140 CONTINUE
1108 C 150 CONTINUE
1109 C RETURN
1110 C END
Top-down view Bottom-up view Flat view
```

Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter



```
hpcviewer
File View Filter Help
Profile: gameSS.00.x Trace: gameSS.00.x Profile: gameSS.00.x Trace: gameSS.00.x Profile: gameSS.00.x Trace: gameSS.00.x Profile: gameSS.01.x Trace: gameSS.01.x

mthlib.f 33
1053 END
1054 C
1055 C*MODULE MTHLIB *DECK MTA88R
1056 C SUBROUTINE MTA88R(A,NA,MB,AB,INCA,INCA)
1057 C
1058 C use omp_lib
1059 C
1060 C IMPLICIT DOUBLE PRECISION(A-H,O-Z)
1061 C
1062 C DIMENSION A(*),B(NA,MB),AB(INAB,MB)
1063 C
1064 C PARAMETER (ZERO=0.0D+00)
1065 C
1066 C* 31 OCT 1979
1067 C*
1068 C*FUNCTION - TO MULTIPLY SYMMETRIC MATRIX A
1069 C* TIMES RECTANGULAR MATRIX B AND GET RECTANGULAR MATRIX AB
1070 C*
1071 C*PARAMETERS
1072 C* A - THE INPUT REAL SYMMETRIC MATRIX OF ORDER NA
1073 C* STORED IN SYMMETRIC STORAGE MODE.
1074 C* B - THE INPUT REAL NA BY MB RECTANGULAR MATRIX
1075 C* NA - THE ORDER OF MATRIX A
1076 C* MB - THE COLUMN DIMENSION OF MATRICES B AND AB
1077 C* AB - THE OUTPUT PRODUCT NA BY MB MATRIX
1078 C* NAB - THE INPUT ROW DIMENSION OF MATRIX AB
1079 C* INCA - ADDRESS DIFFERENCE OF TWO ADJACENT ELEMENTS OF A
1080 C*
1081 C INCA=INCA
1082 C
1083 C PROCESS DIAGONAL ELEMENTS OF INPUT MATRIX A
1084 C
1085 C IJ=1-INC
1086 C DO 120 I=1,NA
1087 C IJ=IJ+INC
1088 C AIJ=A(IJ)
1089 C DO 110 N=1,MB
1090 C AB(I,K)=AIJ*B(I,K)
1091 C 110 CONTINUE
1092 C 120 CONTINUE
1093 C IF(NA.EQ.1) RETURN
1094 C
1095 C
1096 C IJ=1-INC
1097 C DO 150 I=2,NA
1098 C IJ=IJ+INC
1099 C IM1=I-1
1100 C DO 140 J=1,IM1
1101 C IJ=IJ+INC
1102 C AIJ=A(IJ)
1103 C IF(AIJ.EQ.ZERO) GO TO 140
1104 C CALL DAXPY(MB,AIJ,B(I,1),NA,AB(J,1),NAB)
1105 C CALL DAXPY(MB,AIJ,B(J,1),NA,AB(I,1),NAB)
1106 C 140 CONTINUE
1107 C 150 CONTINUE
1108 C RETURN
1109 C END
1110 C
```

Top-down view | Bottom-up view | Flat view

Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter

```
hpcviewer
1096 C
1097     IJ=1-INC
1098     DO 150 I=2,NA
1099         IJ=IJ+INC
1100         IM1=I-1
1101         DO 140 J=1,IM1
1102             IJ=IJ+INC
1103             AIJ=A(IJ)
1104             IF(AIJ.EQ.ZERO) GO TO 140
1105                 CALL DAXPY(MB,AIJ,B(I,1),NA,AB(J,1),NAB)
1106                 CALL DAXPY(MB,AIJ,B(J,1),NA,AB(I,1),NAB)
1107     140     CONTINUE
1108     150 CONTINUE
1109         RETURN
1110         END
Top-down view/Bottom-up view/Flat view
```

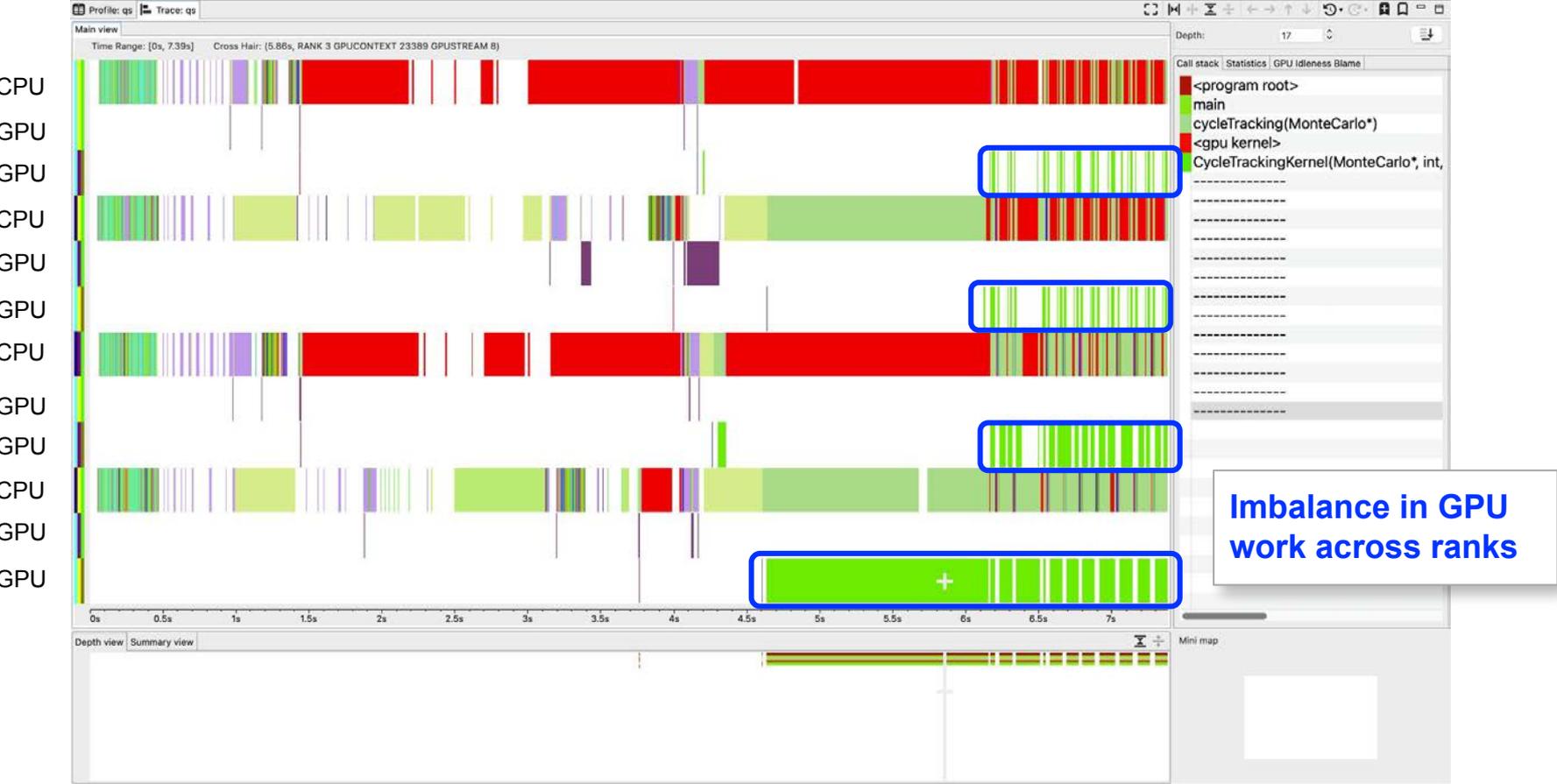
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Case Study: Quicksilver

- Proxy application that represents some elements of LLNL's Mercury code
- Solves a simplified dynamic Monte Carlo particle transport problem
 - Attempts to replicate memory access patterns, communication patterns, and branching or divergence of Mercury for problems using multigroup cross sections
- Parallelization: MPI, OpenMP, CUDA, and HIP
- Performance Issues
 - load imbalance (for canned example)
 - latency bound table look-ups
 - a highly branchy/divergent code path
 - poor vectorization potential

Quicksilver: Trace view



Quicksilver: Detailed analysis within a Kernel using PC Sampling

The screenshot displays the tpcviewer application interface. The top pane shows the source code for CollisionEvent.cc, with line 73 highlighted. The bottom pane shows a performance analysis table with columns for various metrics and a tree view on the left.

| Scope | INS: Sum (I) | GINS: Sum (E) | GINS:STL_ANY: Sum (I) | GINS:STL_ANY: Sum (E) | GINS:STL_IFET: Sum (I) | GINS:STL_IFET: Sum (E) | GINS:STL_IDEP: |
|--|-----------------|---------------|-----------------------|-----------------------|------------------------|------------------------|----------------|
| 14 » [1] cudaLaunchKernel<char> | 1.30e+11 100.0% | | 1.19e+11 100.0% | | 5.27e+09 100.0% | | 9.34e+ |
| 211 » cudaLaunchKernel [qs] | 1.30e+11 100.0% | | 1.19e+11 100.0% | | 5.27e+09 100.0% | | 9.34e+ |
| <gpu kernel> | 1.30e+11 100.0% | | 1.19e+11 100.0% | | 5.27e+09 100.0% | | 9.34e+ |
| CycleTrackingKernel(MonteCarlo*, int, ParticleVault*, ParticleVau... | 1.30e+11 100.0% | 4.08e+07 0.0% | 1.19e+11 100.0% | 3.62e+07 0.0% | 5.27e+09 100.0% | 2.11e+07 0.4% | 9.34e+ |
| CycleTrackingGuts(MonteCarlo*, int, ParticleVault*, Particle... | 1.30e+11 100.0% | 9.03e+09 7.0% | 1.19e+11 100.0% | 9.01e+09 7.6% | 5.24e+09 99.5% | 8.98e+06 0.2% | 9.32e+ |
| [1] CycleTrackingFunction(MonteCarlo*, MC_Particle&, int, P... | 1.36e+10 64.4% | 4.12e+08 0.3% | 7.25e+10 61.1% | 3.65e+08 0.3% | 5.21e+09 98.9% | 1.02e+08 1.9% | 9.25e+ |
| loop at CycleTracking.cc: 118 | 1.35e+10 64.3% | 3.76e+08 0.3% | 7.25e+10 61.1% | 3.34e+08 0.3% | 5.21e+09 98.8% | 9.90e+07 1.9% | 9.24e+ |
| 63 » CollisionEvent(MonteCarlo*, MC_Particle&, unsigned int) [...] | 1.20e+10 40.1% | 4.99e+09 3.8% | 4.44e+10 37.4% | 4.02e+09 3.4% | 3.85e+09 73.1% | 4.89e+08 9.3% | 6.37e+ |
| loop at CollisionEvent.cc: 67 | 1.09e+10 31.5% | 8.15e+08 0.6% | 3.42e+10 28.8% | 6.54e+08 0.6% | 3.54e+09 67.1% | 1.27e+08 2.4% | 5.67e+ |
| loop at CollisionEvent.cc: 71 | 1.05e+10 29.8% | 2.70e+09 2.1% | 3.22e+10 27.1% | 2.06e+09 1.7% | 3.27e+09 62.0% | 2.28e+08 4.3% | 5.33e+ |
| 73 » macroscopicCrossSection(MonteCarlo*, int, int, int, i... | 1.58e+10 27.5% | 1.22e+10 9.4% | 3.01e+10 25.4% | 9.05e+09 8.3% | 3.04e+09 57.7% | 1.79e+09 33.9% | 4.60e+ |
| 41 » NuclearData::getReactionCrossSection(unsigned int, u... | 1.09e+10 16.1% | 1.09e+10 8.4% | 1.79e+10 15.1% | 9.42e+09 7.9% | 1.26e+09 23.8% | 6.68e+08 12.7% | 2.19e+ |
| 253 » [I] NuclearDataReaction::getCrossSection(unsigned ... | 1.09e+09 5.3% | 3.77e+09 2.9% | 5.86e+09 4.9% | 3.32e+09 2.8% | 2.25e+08 4.3% | 8.24e+07 1.6% | 8.06e+ |
| NuclearData.cc: 253 | 1.28e+09 4.8% | 6.28e+09 4.8% | 5.66e+09 4.8% | 5.66e+09 4.8% | 4.76e+08 9.0% | 4.76e+08 9.0% | 6.11e+ |
| NuclearData.cc: 251 | 1.05e+09 1.4% | 1.85e+09 1.4% | 1.64e+09 1.4% | 1.64e+09 1.4% | 8.12e+07 1.5% | 8.12e+07 1.5% | 2.47e+ |
| NuclearData.cc: 248 | 1.61e+09 1.2% | 1.61e+09 1.2% | 1.18e+09 1.0% | 1.18e+09 1.0% | 1.10e+08 2.1% | 1.10e+08 2.1% | 3.62e+ |
| 252 » [I] qs_vector<NuclearDataSpecies>::operator[](int) | 1.29e+09 1.0% | 1.29e+09 1.0% | 1.14e+09 1.0% | 1.14e+09 1.0% | 7.37e+04 0.0% | 7.37e+04 0.0% | 1.24e+ |
| NuclearData.cc: 252 | 1.12e+09 0.9% | 1.12e+09 0.9% | 9.48e+08 0.8% | 9.48e+08 0.8% | 3.44e+05 0.0% | 3.44e+05 0.0% | 2.50e+ |
| 252 » [I] qs_vector<NuclearDataReaction>::size() const | 1.41e+08 0.7% | 9.41e+08 0.7% | 8.17e+08 0.7% | 8.17e+08 0.7% | | | 4.63e+ |
| 253 » [I] qs_vector<NuclearDataReaction>::operator[](int) | 1.36e+08 0.3% | 3.12e+08 0.3% | 2.11e+08 0.3% | 2.11e+08 0.3% | | | 7.37e+ |

Quicksilver: Attribution to Code within a Kernel

```
Scope
  ▲ 14 » [1] cudaLaunchKernel<char>
  ▲ 211 » cudaLaunchKernel [qs]
  ▲ » <gpu kernel>
  ▲ » CycleTrackingKernel(MonteCarlo*, int, ParticleVault*, ParticleVau...
  ▲ 132 » CycleTrackingGuts(MonteCarlo*, int, ParticleVault*, Particle...
  ▲ 26 » [1] CycleTrackingFunction(MonteCarlo*, MC_Particle&, int, P...
  ▲ loop at CycleTracking.cc: 118
  ▲ 63 » CollisionEvent(MonteCarlo*, MC_Particle&, unsigned int) [...
  ▲ loop at CollisionEvent.cc: 67
  ▲ loop at CollisionEvent.cc: 71
  ▲ 73 » macroscopicCrossSection(MonteCarlo*, int, int, int, i...
  ▲ 41 » NuclearData::getReactionCrossSection(unsigned int, u...
  ▶ 253 » [1] NuclearDataReaction::getCrossSection(unsigned ...
    NuclearData.cc: 253
    NuclearData.cc: 251
    NuclearData.cc: 248
  ▶ 252 » [1] qs_vector<NuclearDataSpecies>::operator[](int)
    NuclearData.cc: 252
  ▶ 252 » [1] qs_vector<NuclearDataReaction>::size() const
  ▶ 252 » [1] qs_vector<NuclearDataReaction>::operator[](int)
```

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Work in Progress: Present GPU Metrics using a Donut Graph

- Figure shows forthcoming top-down display of CPU metrics
- Plan similar top-down display of GPU metrics
 - Issues
 - Exposed stalls
 - Memory
 - Pipeline
 - Ifetch
 - ...
 - Hidden stalls

The screenshot displays a performance analysis tool interface. On the left, a code editor shows C++ code for a matrix-vector multiplication loop. The main area features a donut chart titled "Topdown metrics of loop at multiply.c: 838". To the right of the chart is a legend listing various metrics and their percentages. Below the chart is a table showing the scope of the metrics.

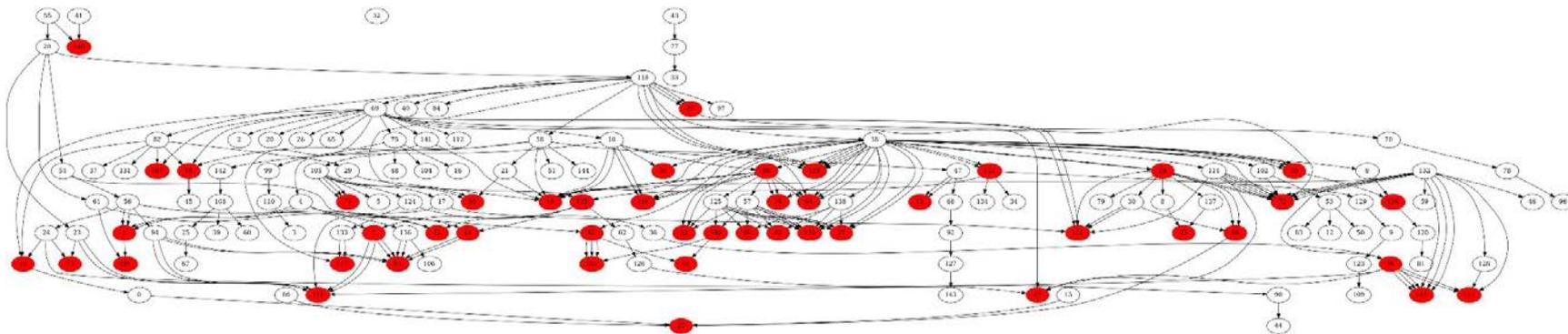
| Scope | Total-Slots (I) |
|---|-----------------|
| 1100 do_spmv | 1.39e+13 3.0% |
| 1163 do_spmv | 1.30e+13 9.2% |
| 1164 do_spmv | 1.19e+13 8.4% |
| 1162 do_spmv | 1.06e+13 7.5% |
| loop at multiply.c: 1066 | 4.03e+12 2.8% |
| loop at multiply.c: 1073 | 3.13e+12 2.2% |
| loop at multiply.c: 1080 | 2.90e+12 2.0% |
| 1081 [I] csr_spmv | 2.90e+12 2.0% |
| 831 <comp impl task> | 2.90e+12 2.0% |
| csr_spmv.extracted | 2.90e+12 2.0% |
| loop at multiply.c: 838 | 2.90e+12 2.0% |
| multiply.c: 840 | 2.78e+12 2.0% |
| multiply.c: 838 | 8.07e+10 0.1% |
| multiply.c: 832 | 3.38e+10 0.0% |
| 831 __kmpe_for_static_init_Bu [libiomp5.so] | |
| __kmpe_for_static_fini [libiomp5.so] | |
| 831 kmnc fork call [libiomp5.so] | |

Topdown metrics of loop at multiply.c: 838

- 25.4% Active (I)
- 25.4% Retiring: Sum (I)
- 3.9% Heavy-Operations: Sum (I)
- 3.9% Uops-Sequencer (I)
- 3.0% Few_Uops_Instructions (I)
- 21.5% Light-Operations (I)
- 17.2% FP-Arithmetic (I)
- 0.0% X87-Use (I)
- 4.6% Instr-Scalar (I)
- 12.6% Instr-Vector (I)
- 0.0% Int-Operations (I)
- 4.4% Mem-operations (I)
- 0.0% Bad-Speculation: Sum (I)
- 0.0% Mispredict: Sum (I)
- 0.0% Machine-Clears (I)
- 74.6% Stall (I)
- 4.1% Frontend-Bound: Sum (I)
- 0.2% Fetch-Latency: Sum (I)
- 0.0% iCache-Miss (I)
- 0.1% iTLB-Miss (I)
- 0.0% unknown_branch (I)
- 3.9% Fetch-Bandwidth (I)
- 70.4% Backend-Bound: Sum (I)
- 34.0% Memory-Bound: Sum (I)
- 2.2% L1-Bound (I)
- 0.2% L2-Bound (I)
- 14.7% L3-Bound (I)

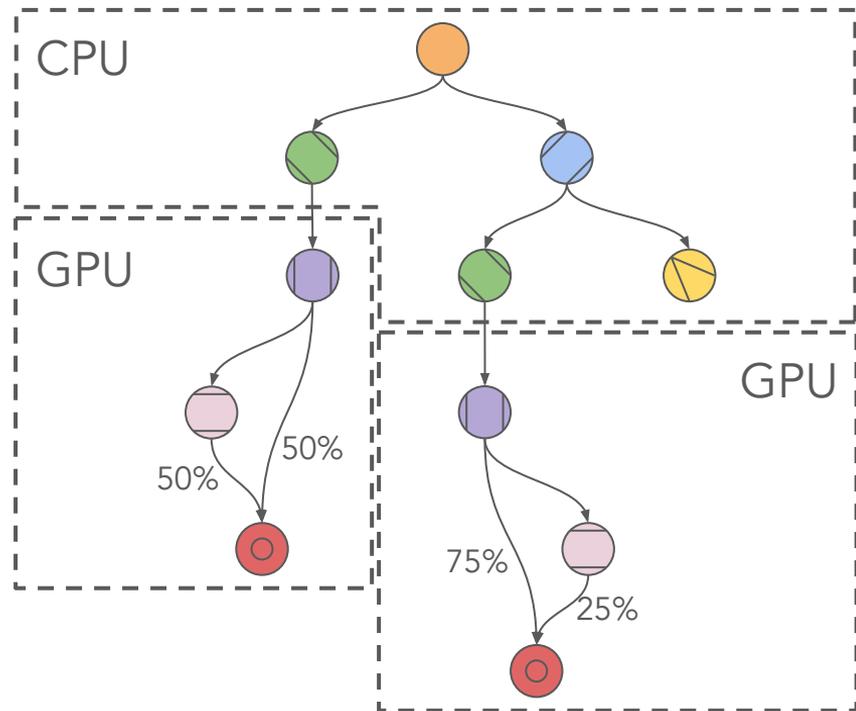
WIP: Enabling Instruction-level Metrics for Complex Kernels

- Calling context is great for visualization, but slow to analyze for complex GPU kernels!
 - Every plausible calling context must be expanded to attribute performance
 - Example: Quicksilver has one kernel with 145 separate GPU functions
 - Call graph below, **red** nodes have multiple plausible calling contexts
 - Sometimes >100,000 calling contexts for a single function, e.g. `cuda_div`



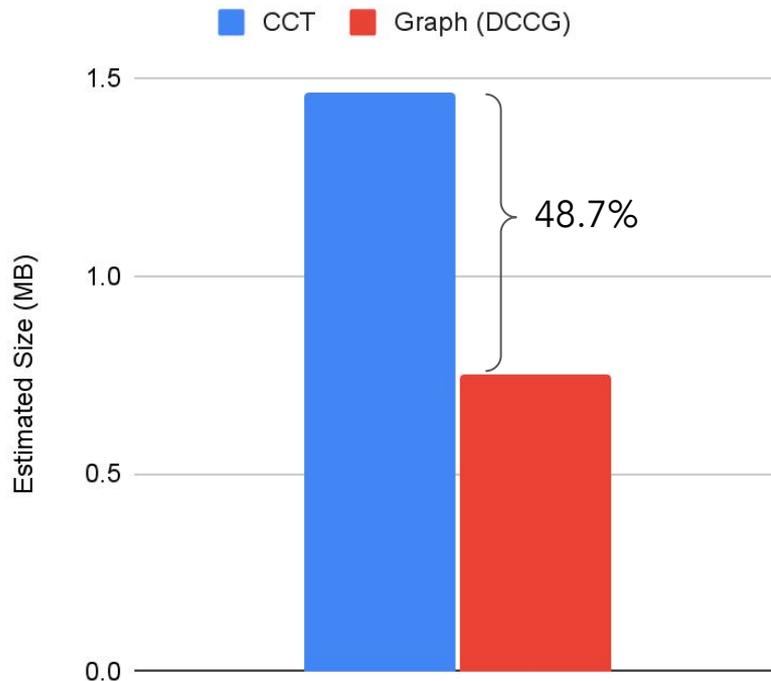
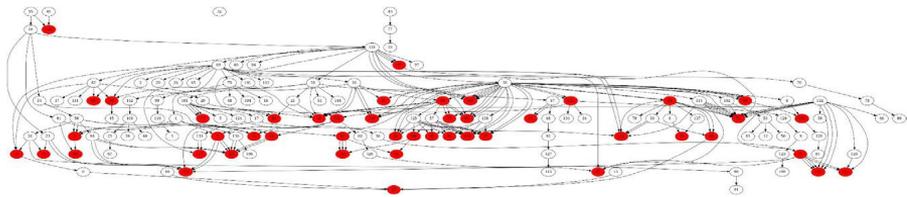
WIP: Enabling Instruction-level Metrics for Complex Kernels

- Next generation: Graph-based calling contexts
 - Performance is attributed to nodes
 - Distribution factors recorded on edges
- Efficient representation for GPU code
 - Avoid reconstruction of duplicate contexts
 - Match GPU measurement capabilities
- Clean model for both CPU and GPU performance
 - Not a hybrid model, one graph handles all

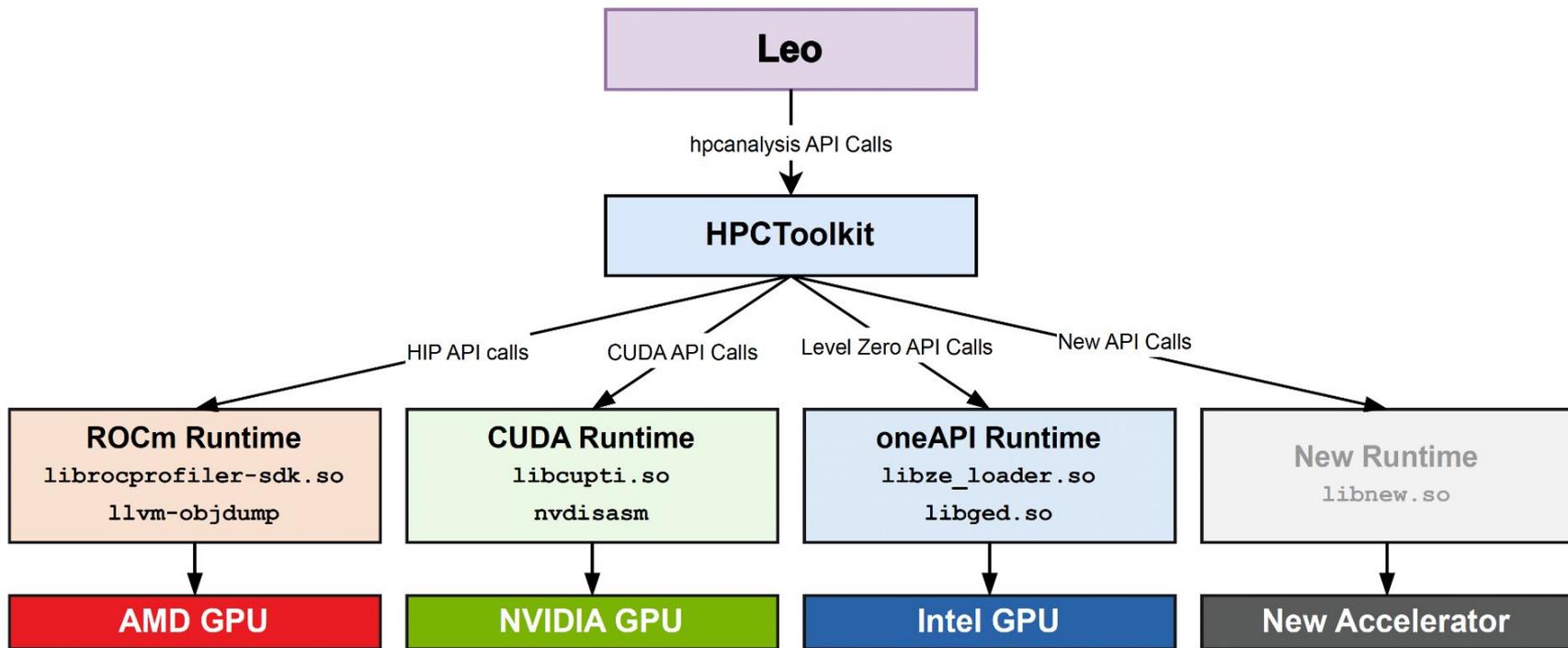


WIP: Enabling Instruction-level Metrics for Complex Kernels

- Case study: Quicksilver
 - 409 unique calling contexts
 - 145 graph nodes (1 per GPU function)
- Preliminary results
 - 48% estimated size reduction
 - 1.3x potential speed up



WIP: Leo - Platform-independent analysis with PC Sampling



WIP: Leo - Platform-independent analysis with PC Sampling

- GPU PC sampling collects a wealth of information
 - Stall reasons (AMD, Intel, NVIDIA)
 - Hidden stalls (NVIDIA, AMD)
 - Register utilization (NVIDIA, AMD)
 - Compute unit utilization (AMD)
 - Pipeline utilization
 - Wavefront occupancy
 - Thread utilization (divergence)
- Leo pinpoints and quantifies root causes of GPU stalls
 - Ranks code locations suffering from GPU instruction stalls
 - Dumps machine code into assembly code
 - Performs dataflow analysis to trace stalls back to potential root causes

WIP: Leo - Sample Report: Analysis of RajaPerfSuite

Database: /data/per-kernel/Algorithm_REDUCE_SUM/amd/hpctoolkit-raja-perf.exe-database
Measurements: /data/per-kernel/Algorithm_REDUCE_SUM/amd/hpctoolkit-raja-perf.exe-measurements

PROGRAM TOTALS

```
-----  
Total Execution Time: 0.0037s  
Total Stall Cycles: 1,873,805,312  
Total GPU Cycles: 1,946,157,056  
Overall Stall Ratio: 96.3%  
Kernels Analyzed: 1  
Kernels Skipped: 0
```

TOP 1 KERNELS BY STALL CYCLES

```
-----  
# Kernel Time (s) Stall Cycles Stall % Occupancy Sample Rate  
-----  
1 .text 0.0020 343,932,928 97.0% 30% (vgpr) 181.3G/s / 28%  
-----
```

KERNEL #1: .text

```
GPU Binary: c59f0354ed847c14b96a717c2709e313.gpubin  
Occupancy: 30% (12/40 waves/CU, limited by vgpr) [VGPRs=40, SGPRs=50, LDS=64B]
```

Leo GPU Performance Analysis

```
-----  
WARNING: Very low profile coverage: 66/15359 (0.4%)
```

```
Kernel: .text Architecture: AMD MI300  
Total Stall Cycles: 423,624,704  
-----
```

STALL ANALYSIS (PC Sampling → Back-slicing → Root Cause)

```
-----  
Stall Location Stall Opcode Root Cause Location Root Opcode Cycles % Total Speedup  
-----  
REDUCE_SUM-Hip.cpp:104:25 s_waitcnt vmcnt(0) <-- REDUCE_SUM-Hip.cpp:104:28 global_load_dwordx2 277,872,640 65.6% 2.71x  
amd_device_functions.h:778:9 s_barrier <-- amd_device_functions.h:778:9 s_barrier 44,040,192 10.4% 1.11x  
functional.hpp:242 s_waitcnt vmcnt(9) <-- block_load_func.hpp:258 global_load_dwordx2 16,777,216 4.0% 1.04x  
block_load_func.hpp:258 global_load_dwordx2 <-- block_load_func.hpp:258 v_addc_co_u32_e32 16,055,075 3.8% 1.04x  
(base) JMC22-S:LEO johnmc$
```

Discussion

- What performance issues do you need help with?
- What would help you investigate these issues?
- Are there any features that you would like added?

HPCToolkit Resources

- Documentation

- User manual for HPCToolkit: <https://hpctoolkit.gitlab.io/hpctoolkit>
- Cheat sheet: <https://gitlab.com/hpctoolkit/hpctoolkit/-/wikis/HPCToolkit-cheat-sheet>
- User manual for hpcviewer: <https://hpctoolkit.gitlab.io/hpctoolkit/users/hpcviewer/hpcviewer.html>
- Tutorial videos
 - <http://hpctoolkit.org/training.html>
 - recorded demo of GPU analysis of Quicksilver: <https://youtu.be/vixa3hGDuGg>
 - recorded tutorial presentation including demo with GPU analysis of GAMESS: <https://vimeo.com/781264043>

- Software

- Download hpcviewer GUI binaries for your laptop, desktop, cluster, or supercomputer
 - OS: Linux, Windows, MacOS
 - Processors: x86_64, aarch64, ppc64le
 - <http://hpctoolkit.org/download.html>
- Install HPCToolkit on your Linux desktop, cluster, or supercomputer using Spack
 - <http://hpctoolkit.org/software.html>